



Parameter	Rating	Units
Blocking Voltage	250	V_P
Load Current	200	mA_{rms} / mA_{DC}
On-Resistance (max)	15	Ω

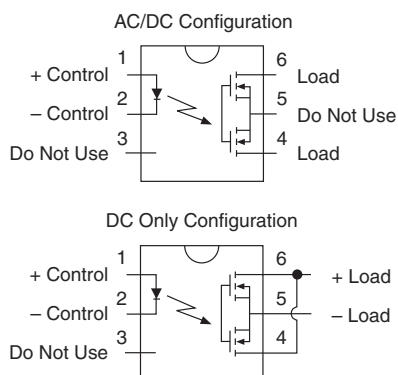
Features

- Integrated active current-limit protection
- Thermal shutdown
- Linear AC or DC operation
- Low power consumption
- Clean, bounce-free switching
- High surge capability
- Low power drive requirements
- Surface mount version available
- Tape & reel packaging available

Applications

- Fault protected switch
- Instrumentation
- Elevator controls
- Industrial controls
- Security
- Peripherals
 - Automatic tuning/balancing
 - Transducer driver
- Pre-driver for large electro-mechanical relays
- Telecom switching
- Medical equipment

Pin Configuration



Description

The CPC1510 is a single-pole, normally open (1-Form-A) Solid State Relay with an integrated current limit feature that can replace electromechanical relays while enhancing the robustness of wireline-interface applications.

The relay is constructed using a GaAlAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology, comprises a photodiode array, switch control with active current limiting circuitry, and MOSFET switches. The active current limit circuitry in the CPC1510 also provides a thermal shutdown feature offering excellent power cross immunity for improved survivability in harsh environments.

These enhancements greatly improve the robustness of end systems using this device compared to systems using relays without the integrated current limit. In addition, the active current limit circuitry enables the CPC1510 to pass FCC 68.302 and other regulatory voltage surge requirements when adequate overvoltage protection is provided. The CPC1510 relay may be used in both unidirectional DC applications as well as bi-directional AC applications.

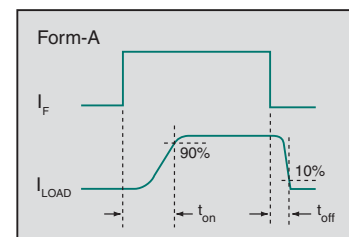
Approvals

- UL Approved Component: File # E76270
- CSA Certified Component: Certificate # 1172007
- EN/IEC 60950-1 Certified Component: Pending

Ordering Information

Part #	Description
CPC1510G	6-Pin 0.250" Wide, Through Hole (50/Tube)
CPC1510GS	6-Pin 0.250" Wide, Surface Mount (50/Tube)
CPC1510GSTR	6-Pin 0.250" Wide, Surface Mount (1000/Reel)

Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	250	V _P
Reverse Input Voltage	5	V
Input LED Current		
Continuous	50	mA
Peak (10ms)	1	A
Input Control Current	10	mA
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate Linearly 1.33 mW/°C

² Derate Linearly 1.67 mW/°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Recommended Operating Conditions

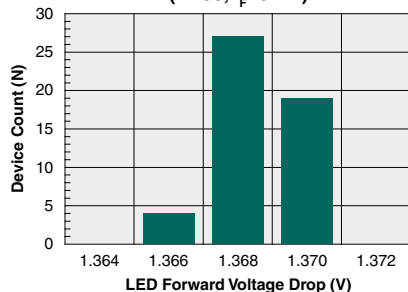
Parameter	Symbol	Configuration	Min	Nominal	Max	Units
Load Current, Continuous	I _L	AC/DC	-	-	200	mA _{rms} / mA _{DC}
		DC-Only	-	-	350	mA _{DC}
Input Control Current	I _F	-	3	5	10	mA
Operating Temperature Range	T _A	-	-40	-	+85	°C

Electrical Characteristics @ 25°C

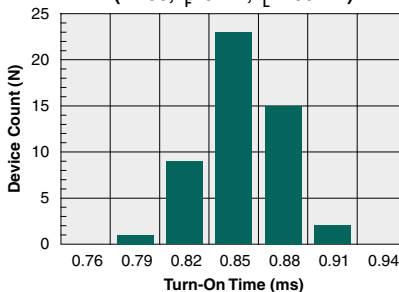
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Current Limit AC/DC Configuration DC Configuration	$I_F=5\text{mA}$, $V_L=\pm 5\text{V}$, $t=5\text{ms}$	I_{LMT}	300	366	450	mA_p
			600	730	920	
On-Resistance AC/DC Configuration DC Configuration	$I_F=5\text{mA}$, $I_L=100\text{mA}$	R_{ON}	6	13	15	Ω
			1.5	3.3	3.75	
Off-State Leakage Current	$V_L=200\text{V}$	I_{LEAK}	-	$1.3e^{-5}$	1	μA
Switching Speeds Turn-On Turn-Off	$I_F=5\text{mA}$, $I_L=10\text{mA}$, $V_L=10\text{V}$	t_{on}	-	0.845	2	ms
		t_{off}		0.26		
Output Capacitance	$I_F=0\text{mA}$, $V_L=1\text{V}$, $f=1\text{MHz}$	C_O	-	205	-	pF
	$I_F=0\text{mA}$, $V_L=50\text{V}$, $f=1\text{MHz}$			65		
Input Characteristics						
Input Control Current to Activate	$I_L=100\text{mA}$	I_F	-	-	2	mA
Input Control Current to Deactivate	$I_L=100\text{mA}$	I_F	0.2	-	-	mA
LED Forward Voltage	$I_F=5\text{mA}$	V_F	1.15	1.37	1.5	V
Common Characteristics						
Input to Output Capacitance	-	$C_{I/O}$	-	3	-	pF

PERFORMANCE DATA*

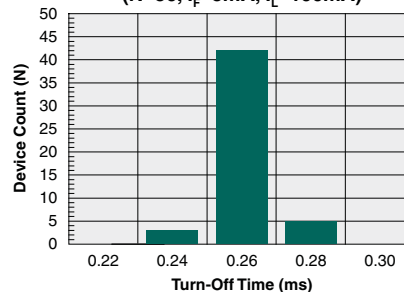
Typical LED Forward Voltage Drop
($N=50$, $I_F=5\text{mA}$)



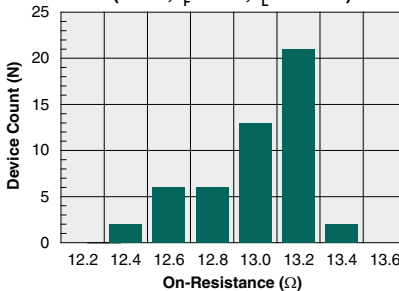
Typical Turn-On Time
($N=50$, $I_F=5\text{mA}$, $I_L=100\text{mA}$)



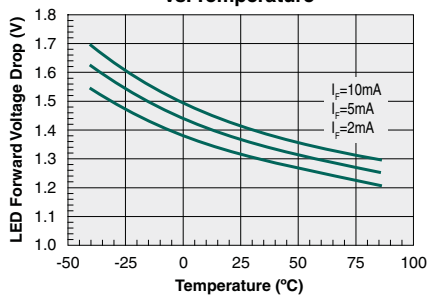
Typical Turn-Off Time
($N=50$, $I_F=5\text{mA}$, $I_L=100\text{mA}$)



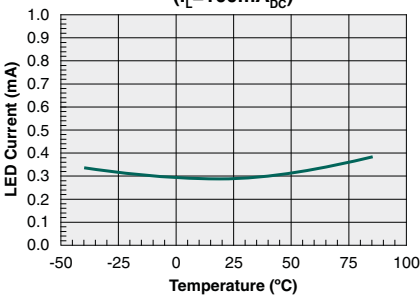
Typical On-Resistance
($N=50$, $I_F=5\text{mA}$, $I_L=100\text{mA}$)



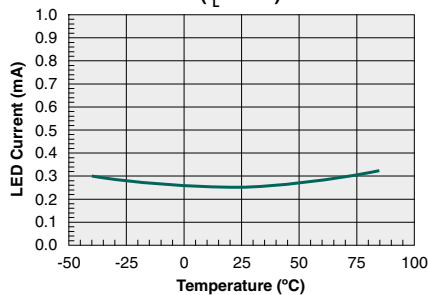
Typical LED Forward Voltage Drop
vs. Temperature



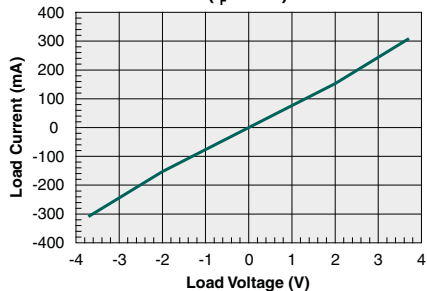
Typical I_F for Switch Operation
vs. Temperature
($I_L=100\text{mA}_{DC}$)



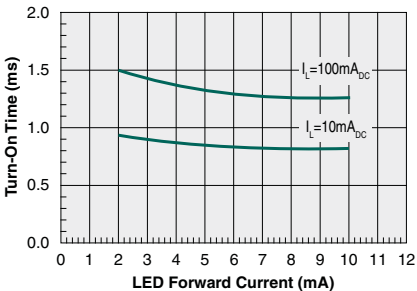
Typical I_F for Switch Dropout
vs. Temperature
($I_L=1\text{mA}$)



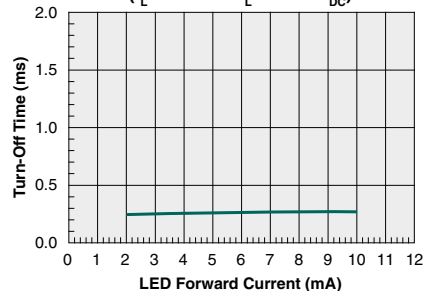
Typical Load Current vs. Load Voltage
($I_F=5\text{mA}$)



Typical Turn-On Time
vs. LED Forward Current

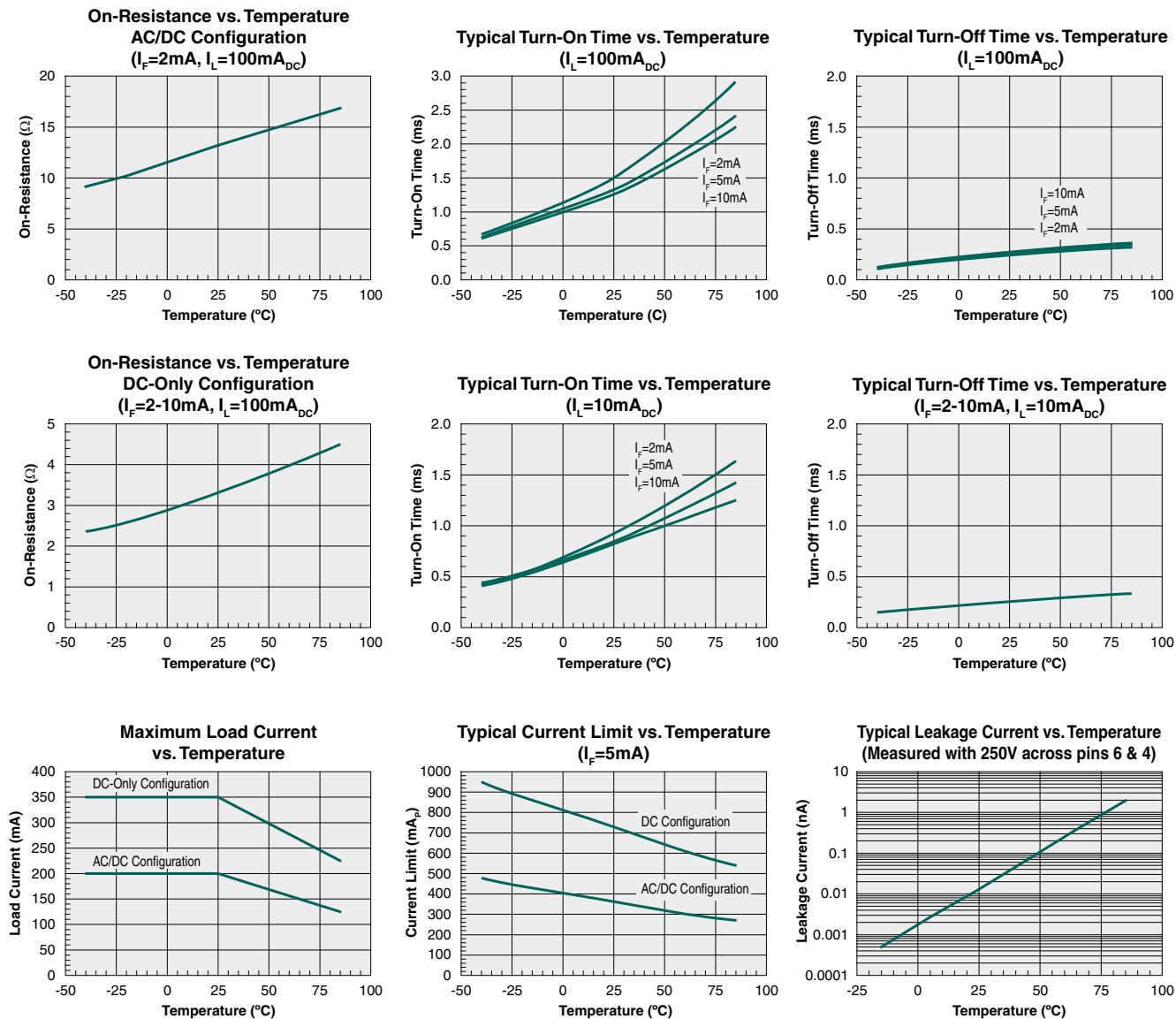


Typical Turn-Off Time
vs. LED Forward Current
($I_L=10\text{mA}$ & $I_L=100\text{mA}_{DC}$)



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
CPC1510GS	MSL 1

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum dwell time the body temperature of these surface mount devices may be ($T_C - 5$)°C or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature (T_C)	Dwell Time (t_p)	Max Reflow Cycles
CPC1510GS	250°C	30 seconds	3

The maximum wave soldering conditions of the through-hole devices is provided in the following table. Dwell time is the time it takes for the pins to pass through both waves.

Device	Maximum Wave Temperature	Body Temperature	Dwell Time	Wave Cycles
CPC1510G	260°C	250°C	10 seconds	1

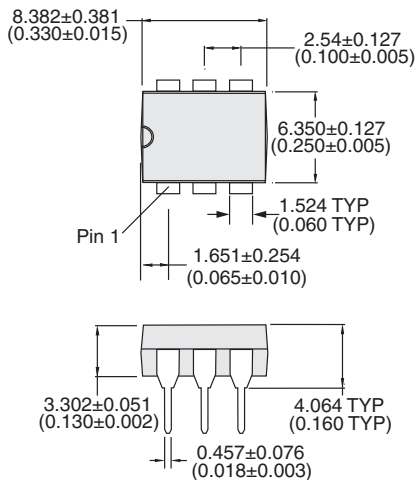
Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents that are Chlorine, Bromine, Fluorine, or Iodine-based.

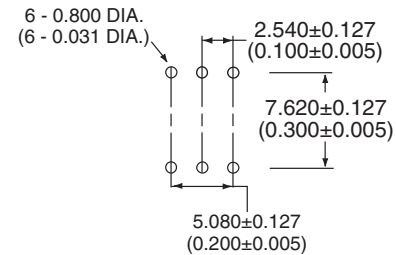


Mechanical Dimensions

CPC1510G

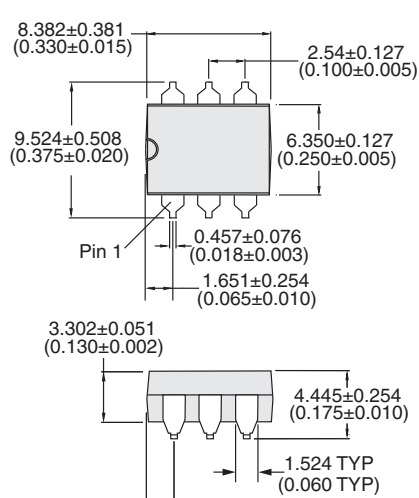


PCB Hole Pattern

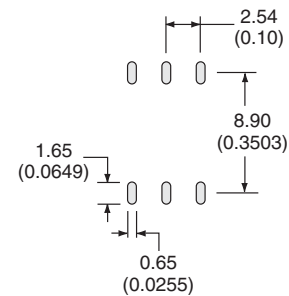


Dimensions
mm
(inches)

CPC1510GS

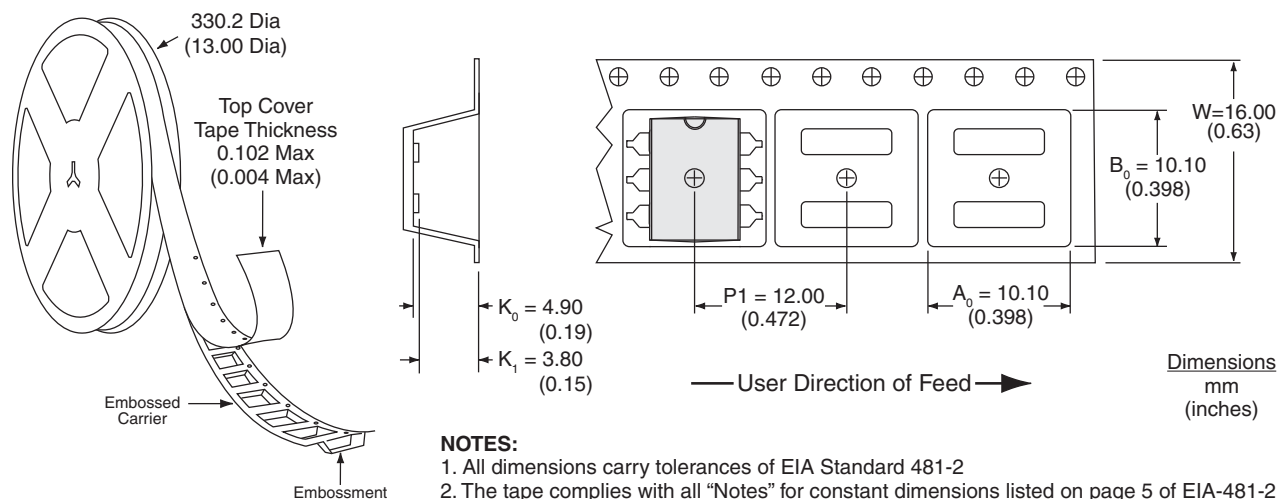


PCB Land Pattern



Dimensions
mm
(inches)

CPC1510GSTR Tape & Reel



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