

Parameter	Rating	Units
Blocking Voltage	350	$V_P$
Load Current	120	$mA_{rms} / mA_{DC}$
On-Resistance (max)	35	$\Omega$

### Features

- 3750V<sub>rms</sub> Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- Small 8-Pin Package
- Machine Insertable, Wave Solderable
- Surface Mount and Tape & Reel Versions Available

### Applications

- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

### Description

The TS120 integrated circuit device combines a 350V normally open (1-Form-A) relay with a Darlington transistor optocoupler in a single package. The relay uses optically coupled MOSFET technology to provide 3750V<sub>rms</sub> of input to output isolation.

Its optically coupled relay outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient GaAIAs infrared LED.

The TS120 enables telecom circuit designers to combine two discrete functions in a single component that uses less space than traditional discrete component solutions.

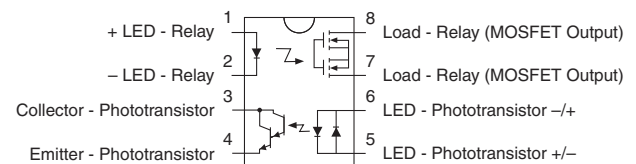
### Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950 Certified Component:  
TUV Certificate: B 10 05 49410 006

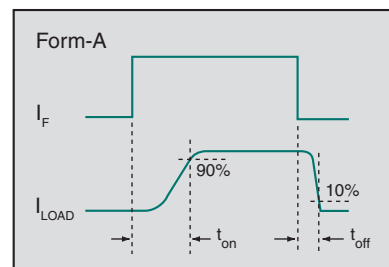
### Ordering Information

Part #	Description
TS120	8-Pin DIP (50/Tube)
TS120P	8-Pin Flatpack (50/Tube)
TS120PTR	8-Pin Flatpack (1000/Reel)
TS120S	8-Pin Surface Mount (50/Tube)
TS120STR	8-Pin Surface Mount (1000/Reel)

### Pin Configuration



### Switching Characteristics of Normally Open Devices



**Absolute Maximum Ratings @ 25°C**

Parameter	Ratings	Units
Blocking Voltage	350	V <sub>P</sub>
Input Power Dissipation <sup>1</sup>	150	mW
Input Control Current, Relay Peak (10ms)	50 1	mA A
Input Control Current, Detector	100	mA
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate linearly 6.67 mW / °C

*Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.*

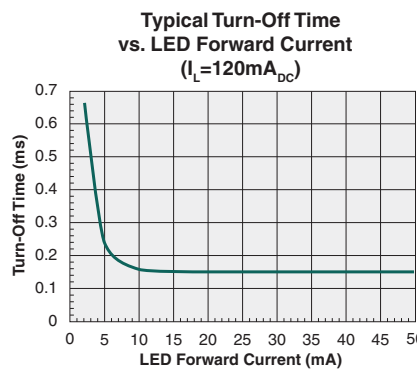
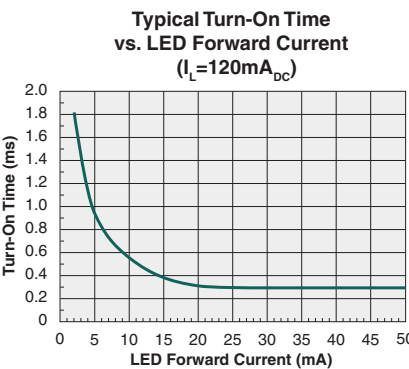
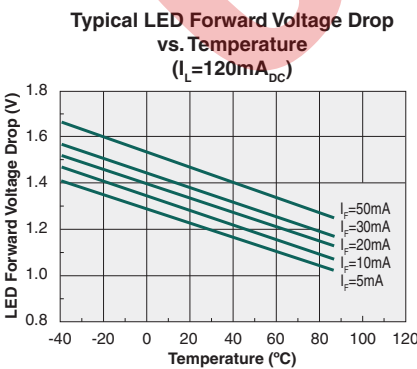
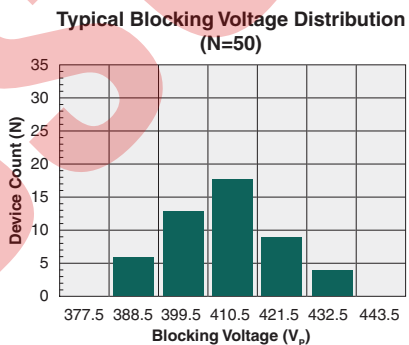
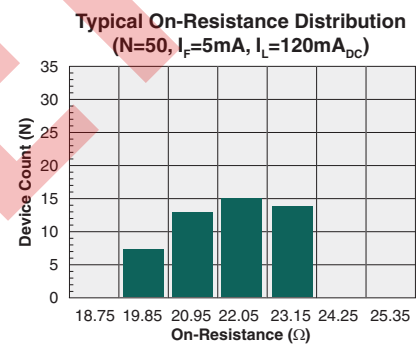
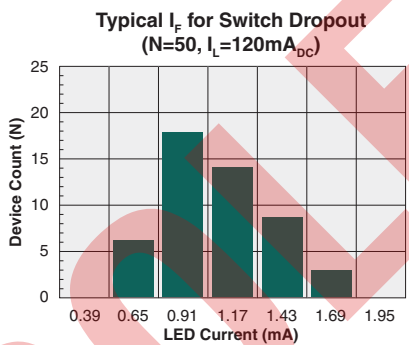
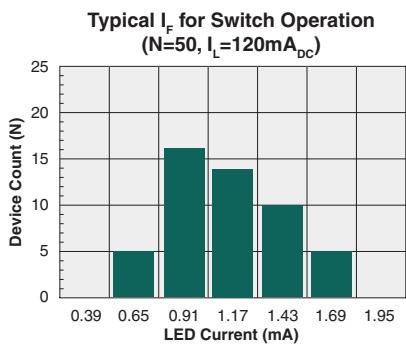
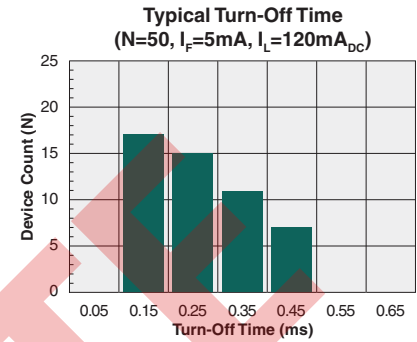
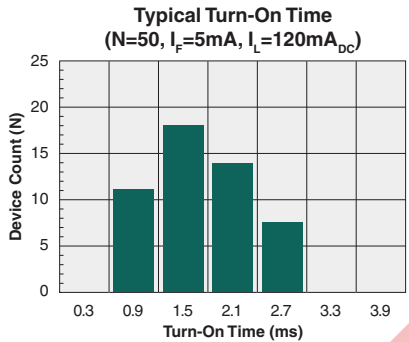
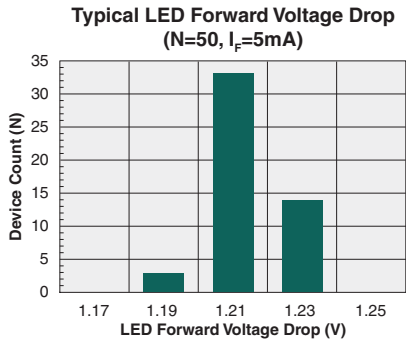
**Electrical Characteristics @25°C: Relay Section**

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Load Current						
Continuous	-	I <sub>L</sub>	-	-	120	mA <sub>rms</sub> / mA <sub>DC</sub>
Peak	t=10ms	I <sub>LPK</sub>	-	-	±350	mA <sub>P</sub>
On-Resistance	I <sub>L</sub> =120mA	R <sub>ON</sub>	-	23	35	Ω
Off-State Leakage Current	V <sub>L</sub> =350V	I <sub>LEAK</sub>	-	-	1	μA
<b>Switching Speeds</b>						
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	-	3	ms
Turn-Off	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>off</sub>	-	-	3	ms
Output Capacitance	V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	25	-	pF
<b>Input Characteristics</b>						
Input Control Current to Activate	I <sub>L</sub> =120mA	I <sub>F</sub>	-	-	5	mA
Input Control Current to Deactivate	-	I <sub>F</sub>	0.4	0.7	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Voltage	-	V <sub>R</sub>	-	-	5	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Common Characteristics</b>						
Input to Output Capacitance	-	C <sub>I/O</sub>	-	3	-	pF

**Electrical Characteristics @25°C: Detector Section**

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Phototransistor Blocking Voltage	I <sub>C</sub> =10μA	BV <sub>CEO</sub>	20	50	-	V
Phototransistor Dark Current	V <sub>CE</sub> =5V, I <sub>F</sub> =0mA	I <sub>CEO</sub>	-	100	1000	nA
Saturation Voltage	I <sub>C</sub> =0.15mA, I <sub>F</sub> =0.05mA	V <sub>SAT</sub>	-	0.5	0.8	V
Current Transfer Ratio	I <sub>F</sub> =0.05mA, V <sub>CE</sub> =0.8V	CTR	300	1000	-	%
<b>Input Characteristics</b>						
Input Control Current	I <sub>C</sub> =2mA, V <sub>CE</sub> =0.5V	I <sub>F</sub>	-	1	2	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Input to Output Capacitance	-	-	-	3	-	pF
Isolation, Input to Output	-	V <sub>I/O</sub>	3750	-	-	V <sub>rms</sub>

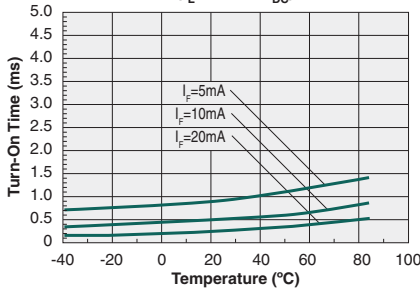
RELAY PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*



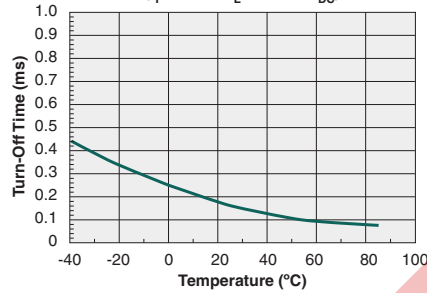
\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

RELAY PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*

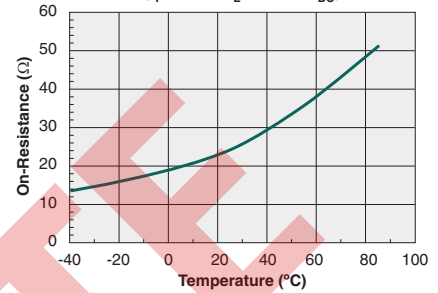
Typical Turn-On Time vs. Temperature  
( $I_L=120\text{mA}_{DC}$ )



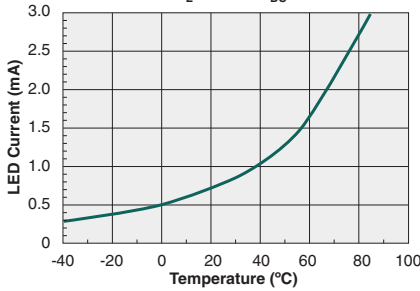
Typical Turn-Off Time vs. Temperature  
( $I_F=5\text{mA}, I_L=120\text{mA}_{DC}$ )



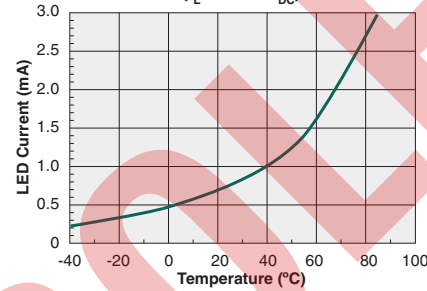
Typical On-Resistance vs. Temperature  
( $I_F=5\text{mA}, I_L=120\text{mA}_{DC}$ )



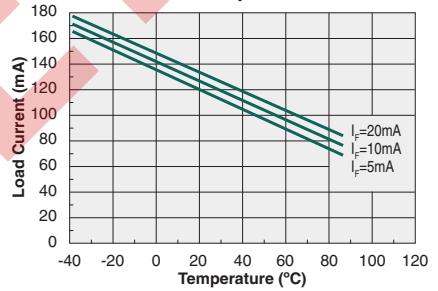
Typical  $I_F$  for Switch Operation vs. Temperature  
( $I_L=120\text{mA}_{DC}$ )



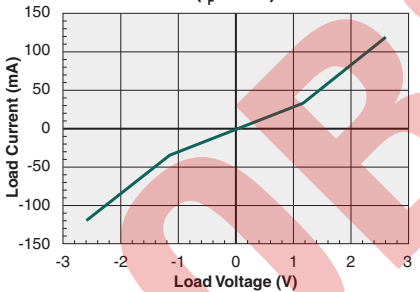
Typical  $I_F$  for Switch Dropout vs. Temperature  
( $I_L=120\text{mA}_{DC}$ )



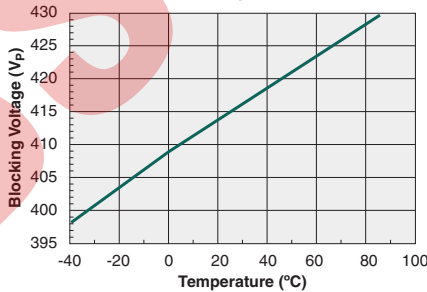
Maximum Load Current vs. Temperature



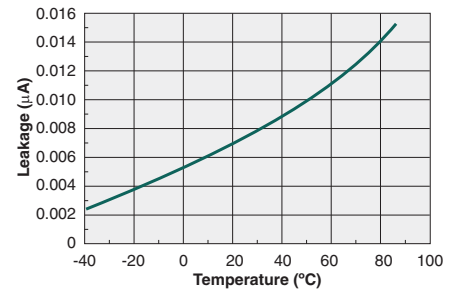
Typical Load Current vs. Load Voltage  
( $I_F=5\text{mA}$ )



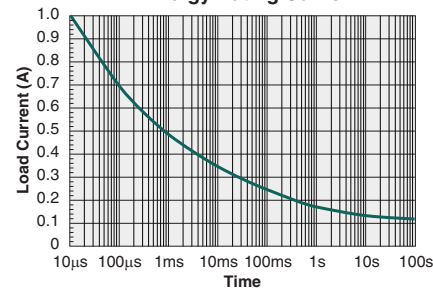
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature Measured across Pins 7&8

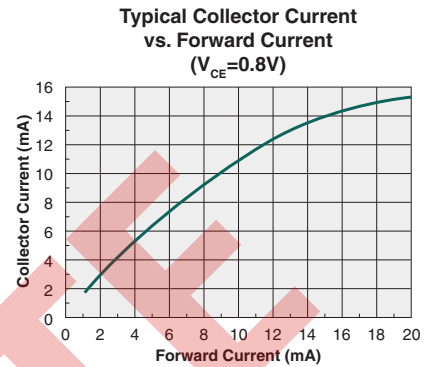
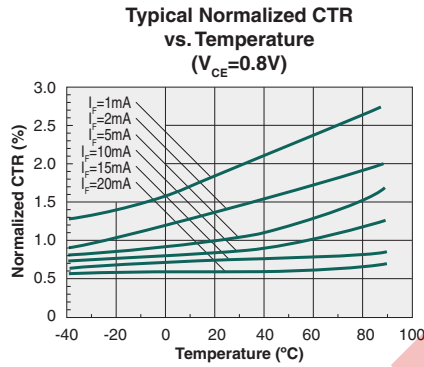
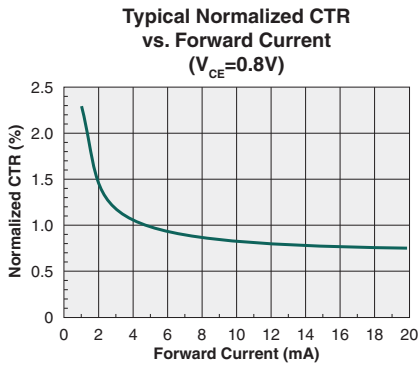


Energy Rating Curve



\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

**DETECTOR PERFORMANCE DATA @25°C (Unless Otherwise Noted)\***



OBSOLETE

\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

**Manufacturing Information**

**Moisture Sensitivity**



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
TS120 / TS120P / TS120S	MSL 1

**ESD Sensitivity**



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

**Reflow Profile**

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
TS120 / TS120S	250°C for 30 seconds
TS120P	260°C for 30 seconds

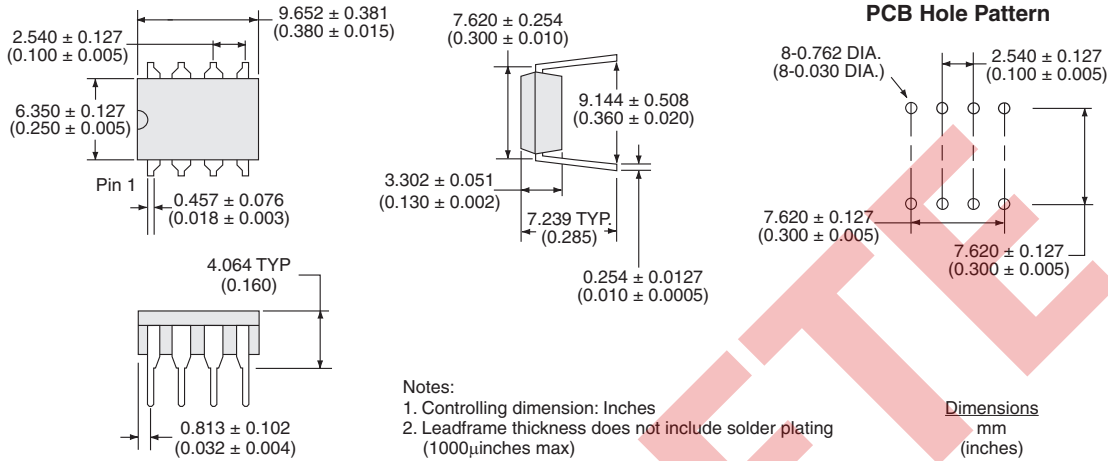
**Board Wash**

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

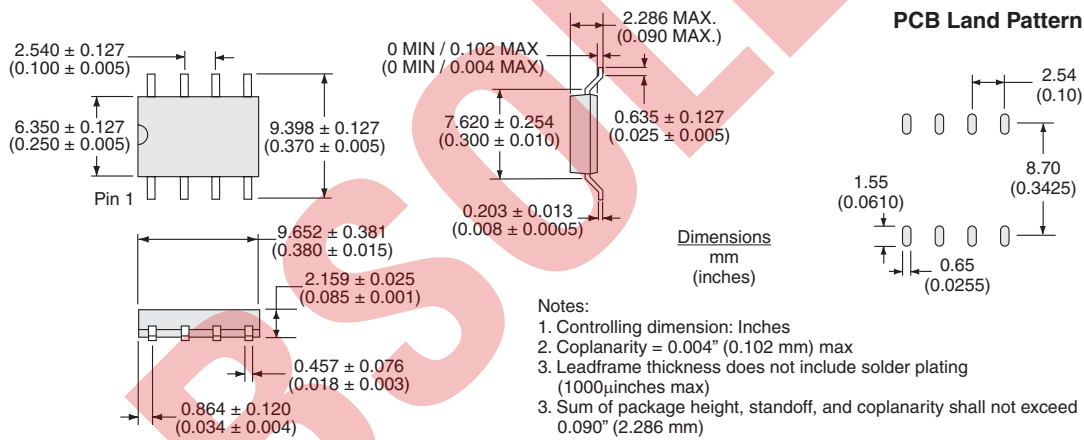


**Mechanical Dimensions**

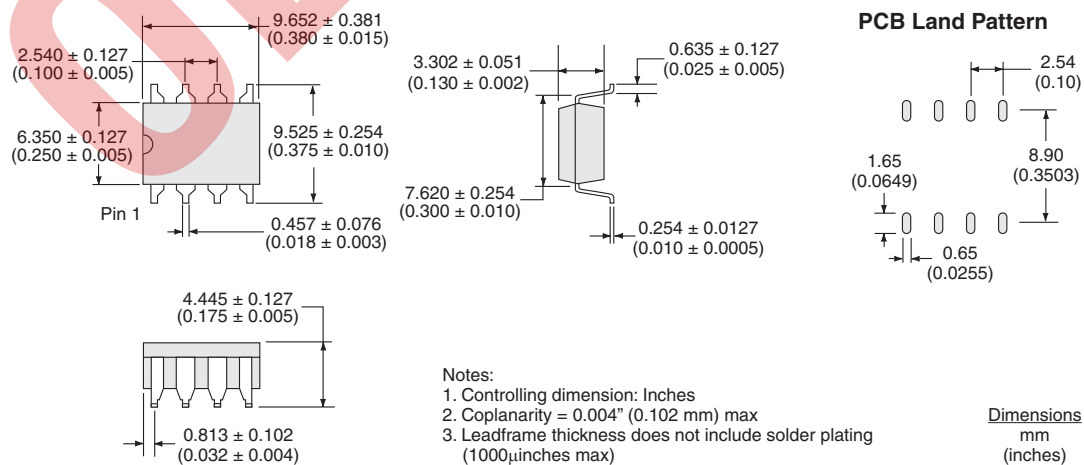
**TS120**



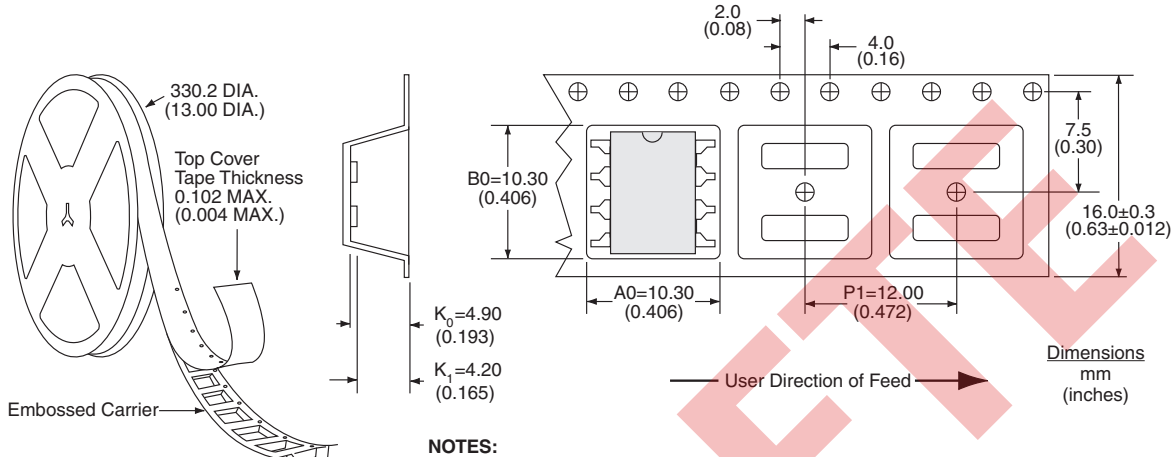
**TS120P**



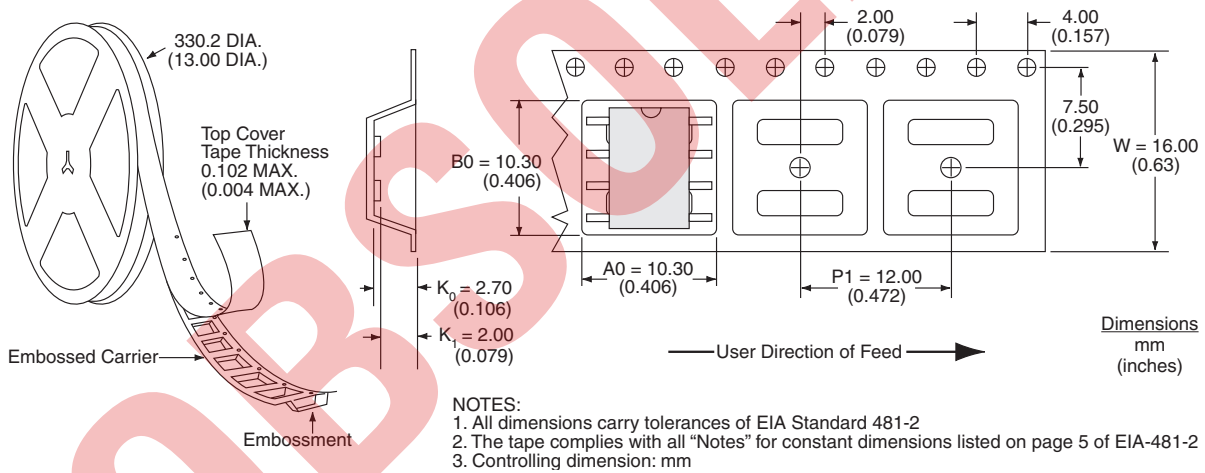
**TS120S**



TS120STR Tape & Reel



TS120PTR Tape & Reel



For additional information please visit our website at: [www.ixysic.com](http://www.ixysic.com)

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