

# SP4338-02WTG

5V, 0.18pF, 15kV, 2CH-Bidirectional TVS, Ultra Low Capacitance ESD protection

HF RoHS Pb

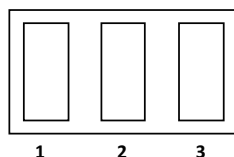


## Description

The SP4338-02WTG provides ultra-low capacitance, bidirectional and a high level of protection for 2 channel electronic equipment that may experience destructive electrostatic discharges (ESD). The typical capacitance of 0.18pF helps ensure excellent signal integrity on the most challenging consumer electronics interfaces, such as DisplayPort interfaces, Thunderbolt and high speed USB.

It can safely absorb repetitive ESD strikes at  $\pm 15\text{kV}$  (contact discharge, IEC 61000-4-2) without performance degradation and safely dissipate 7A of 8/20 $\mu\text{s}$  surge current (IEC 61000-4-5 2<sup>nd</sup> edition).

## Pinout

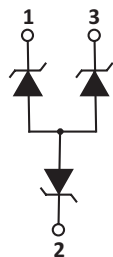


Bottom View

## Features

- ESD, IEC 61000-4-2,  $\pm 15\text{kV}$  contact/air
- EFT, IEC 61000-4-4, 40A (5/50ns)
- Maximum surge tolerance, IEC 61000-4-5, 2<sup>nd</sup> Edition, 7A (8/20 $\mu\text{s}$ )
- Low leakage current of 0.1 $\mu\text{A}$  (MAX) at 5V
- Ultra low capacitance of 0.18pF (Typ @  $V_R=0\text{V}$ )
- Space efficient 0201
- Halogen-free, lead-free and RoHS compliant

## Functional Block Diagram



## Applications

- USB 2.0 to 4.0
- Computing
- MIPI
- DisplayPort
- Thunderbolt
- S-ATA
- Cell phone

### Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

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## Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$I_{PP}$	Peak Current ( $t_p=8/20\mu s$ )	7.0	A
$T_{OP}$	Operating Temperature	-40 to 125	°C
$T_{STOR}$	Storage Temperature	-55 to 150	°C

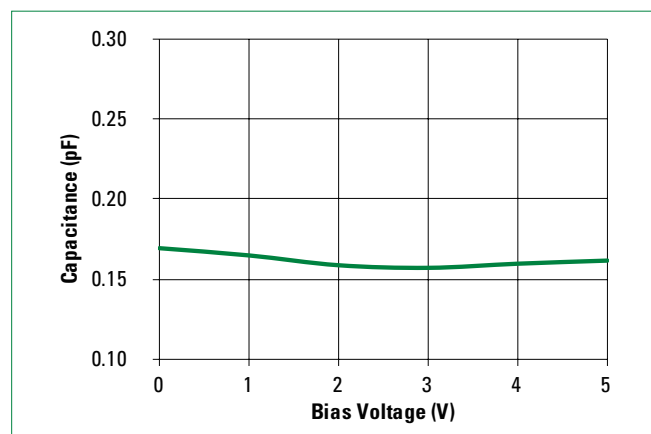
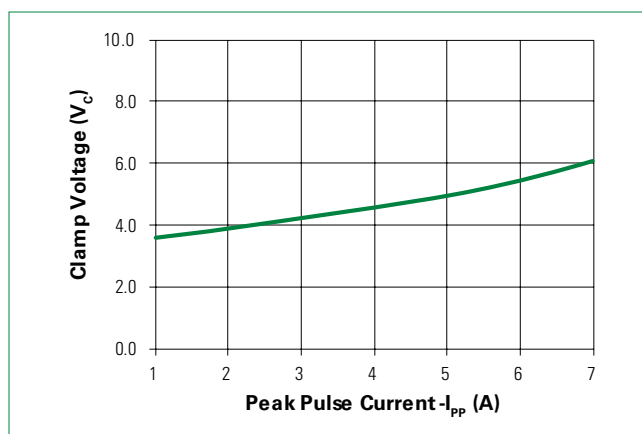
**CAUTION:** Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

## Electrical Characteristics ( $T_{OP}=25^\circ C$ )

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	$V_{RWM}$				5	V
Breakdown Voltage	$V_{BR}$	$I_R=1mA$	6.0	8.0		V
Reverse Leakage Current	$I_{LEAK}$	$V_R=5V$		10	100	nA
Clamp Voltage <sup>1</sup>	$V_C$	$I_{PP}=1A, t_p=8/20\mu s$		3.3		V
		$I_{PP}=7A, t_p=8/20\mu s$		6.0		V
Dynamic Resistance <sup>2</sup>	$R_{DYN}$	TLP, $t_p=100ns$		0.23		$\Omega$
ESD Withstand Voltage <sup>1,3</sup>	$V_{ESD}$	IEC 61000-4-2 (Contact Discharge)	$\pm 15$			kV
		IEC 61000-4-2 (Air Discharge)	$\pm 15$			kV
Diode Capacitance <sup>1</sup>	$C_{IO-IO}$	Reverse Bias=0V, $f=1MHz$		0.18		pF

**Note:**

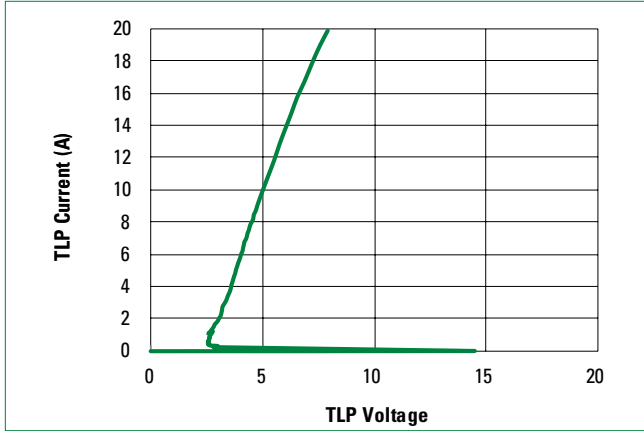
- Parameter is guaranteed by design and/or component characterization.
- Transmission Line Pulse (TLP) with 100ns width, 0.2ns rise time, and average window  $t_1=70ns$  to  $t_2=90ns$
- Device stressed with ten non-repetitive ESD pulses.

**Capacitance vs. Reverse Bias****Clamping Voltage vs  $I_{PP}$** 

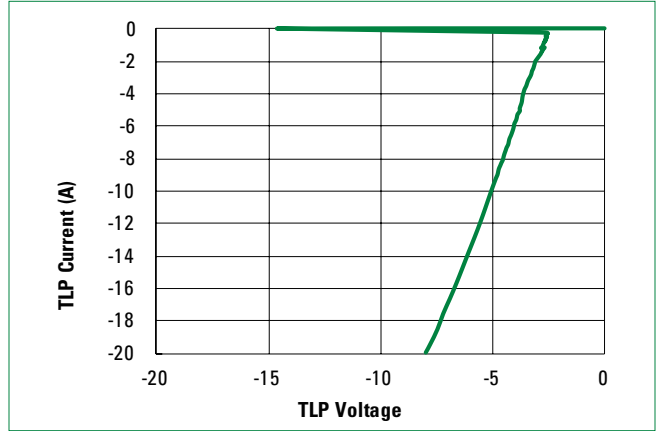
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Positive Transmission Line Pulsing (TLP) Plot



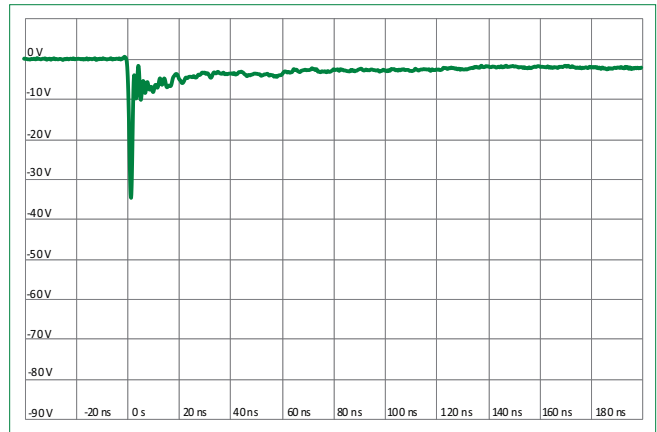
Negative Transmission Line Pulsing (TLP) Plot



IEC 61000-4-2 +8 kV Contact ESD Clamping Voltage



IEC 61000-4-2 -8 kV Contact ESD Clamping Voltage



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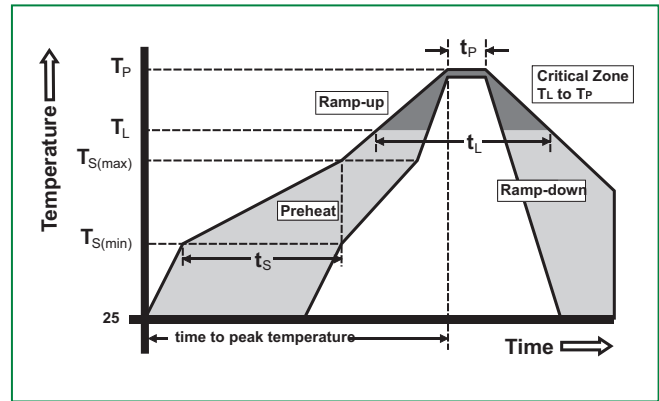
## Soldering Parameters

<b>Reflow Condition</b>		Pb – Free assembly
<b>Pre Heat</b>	- Temperature Min ( $T_{s(min)}$ )	150°C
	- Temperature Max ( $T_{s(max)}$ )	200°C
	- Time (min to max) ( $t_s$ )	60 – 120 secs
<b>Average ramp up rate (Liquidus) Temp (<math>T_L</math>) to peak</b>		3°C/second max
<b><math>T_{s(max)}</math> to <math>T_L</math> - Ramp-up Rate</b>		3°C/second max
<b>Reflow</b>	- Temperature ( $T_L$ ) (Liquidus)	217°C
	- Temperature ( $t_L$ )	60 – 150 seconds
<b>Peak Temperature (<math>T_p</math>)</b>		260 <sup>+0/-5</sup> °C
<b>Time within 5°C of actual peak Temperature (<math>t_p</math>)</b>		30 seconds
<b>Ramp-down Rate</b>		6°C/second max
<b>Time 25°C to peak Temperature (<math>T_p</math>)</b>		8 minutes Max.
<b>Do not exceed</b>		260°C

## Ordering Information

Part Number	Package	Min. Order Qty.
SP4338-02WTG	Flipchip	10000

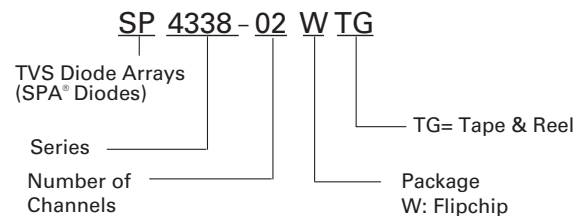
## Part Marking System



## Product Characteristics

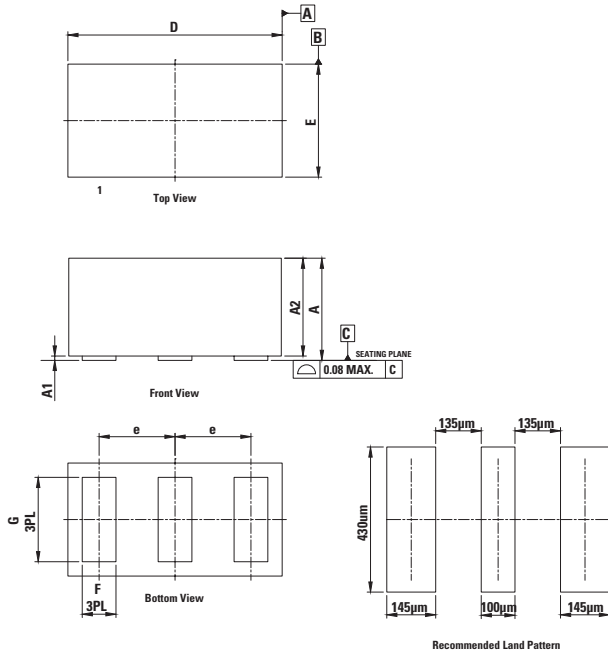
<b>Lead Plating</b>	Matte Tin Plating
<b>Lead material</b>	Copper Bump
<b>Flammability</b>	UL Recognized compound meeting flammability rating V-0

## Part Numbering System

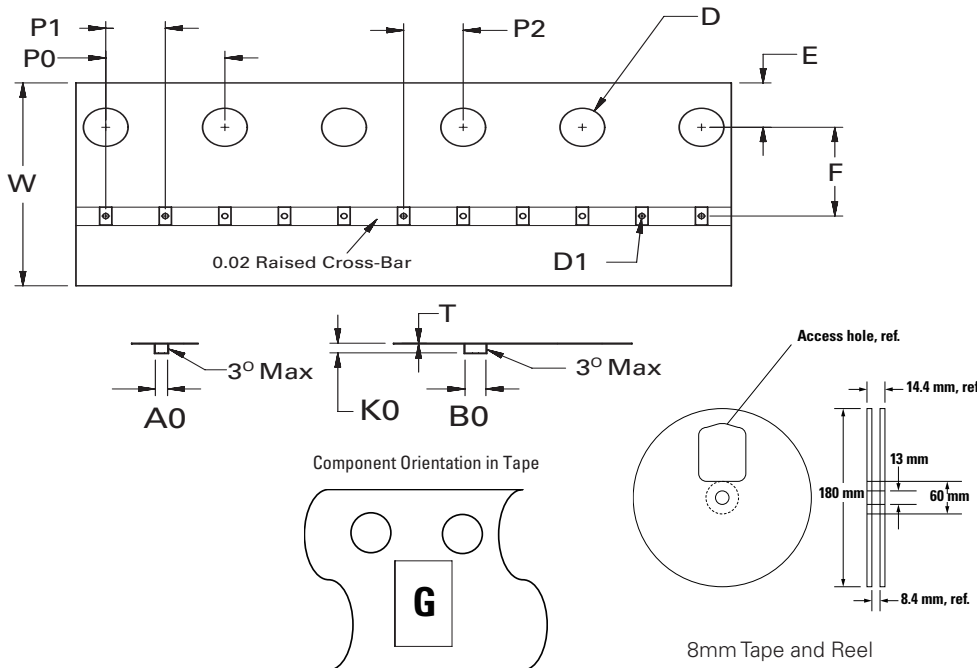


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**Package Dimensions — Flipchip**

Symbol	Millimeters			Inches		
	Min	Nom	Max	Min	Nom	Max
A	0.234	0.250	0.266	0.0092	0.0098	0.0105
A1	0.008	0.011	0.014	0.0003	0.0004	0.0006
A2	0.226	0.239	0.242	0.0089	0.0094	0.0095
e	0.225 BSC			0.0089 BSC		
D	0.605	0.620	0.635	0.0238	0.0244	0.0250
E	0.305	0.320	0.335	0.0120	0.0126	0.0132
F	0.094	0.100	0.106	0.0037	0.0039	0.0042
G	0.244	0.250	0.256	0.0096	0.0098	0.0101

**Embossed Carrier Tape & Reel Specification — Flipchip**

Symbol	Millimeters
A0	0.38+/-0.03
B0	0.68+/-0.03
D	∅ 1.50 + 0.10
D1	∅ 0.17 +/- 0.05
E	1.75+/-0.10
F	3.50+/-0.05
K0	0.30+/-0.03
P0	4.00+/-0.10
P1	2.00+/-0.05
P2	2.00+/-0.05
W	8.00+0.30/-0.10
T	0.23+/-0.02

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