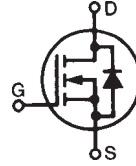


# High Voltage Power MOSFET

**IXTA05N100HV**  
**IXTA05N100**  
**IXTP05N100**

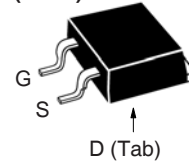
$V_{DSS} = 1000V$   
 $I_{D25} = 750mA$   
 $R_{DS(on)} \leq 17\Omega$

N-Channel Enhancement Mode  
Avalanche Rated

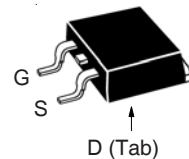


Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ C$ to $150^\circ C$	1000	V
$V_{DGR}$	$T_J = 25^\circ C$ to $150^\circ C$ , $R_{GS} = 1M\Omega$	1000	V
$V_{GSS}$	Continuous	$\pm 30$	V
$V_{GSM}$	Transient	$\pm 40$	V
$I_{D25}$	$T_C = 25^\circ C$	750	mA
$I_{DM}$	$T_C = 25^\circ C$ , Pulse Width Limited by $T_{JM}$	3	A
$I_A$	$T_C = 25^\circ C$	1	A
$E_{AS}$	$T_C = 25^\circ C$	100	mJ
$dv/dt$	$I_S \leq I_{DM}$ , $V_{DD} \leq V_{DSS}$ , $T_J = 150^\circ C$	3	V/ns
$P_D$	$T_C = 25^\circ C$	40	W
$T_J$		-55 ... +150	$^\circ C$
$T_{JM}$		150	$^\circ C$
$T_{stg}$		-55 ... +150	$^\circ C$
$T_L$	Maximum Lead Temperature for Soldering	300	$^\circ C$
$T_{SOLD}$	1.6 mm (0.062in.) from Case for 10s	260	$^\circ C$
$M_d$	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in
<b>Weight</b>	TO-220	3.0	g
	TO-263	2.5	g
	TO-263HV	2.5	g

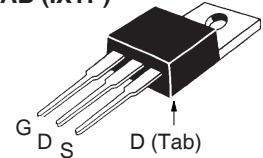
TO-263HV (IXTA)



TO-263 AA (IXTA)



TO-220AB (IXTP)



G = Gate      D = Drain  
S = Source    Tab = Drain

## Features

- High Voltage Package (TO-263HV)
- Fast Switching Times
- Avalanche Rated
- $R_{ds(on)}$  HDMOS™ Process
- Rugged Polysilicon Gate Cell structure
- Extended FBSOA

## Advantages

- High Power Density
- Space Savings

## Applications

- Switch-Mode and Resonant-Mode Power Supplies
- Flyback Inverters
- DC Choppers

Symbol	Test Conditions ( $T_J = 25^\circ C$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0V$ , $I_D = 250\mu A$	1000		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	2.5		4.5 V
$I_{GSS}$	$V_{GS} = \pm 30V$ , $V_{DS} = 0V$			$\pm 100$ nA
$I_{DSS}$	$V_{DS} = V_{DSS}$ , $V_{GS} = 0V$ $T_J = 125^\circ C$			25 $\mu A$ 500 $\mu A$
$R_{DS(on)}$	$V_{GS} = 10V$ , $I_D = 375mA$ , Note 1			17 $\Omega$

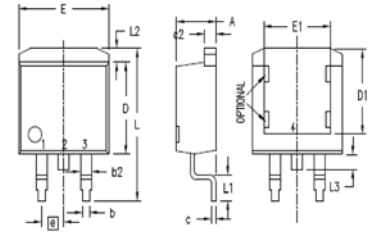
Symbol	Test Conditions (T <sub>J</sub> = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
<b>g<sub>fs</sub></b>	V <sub>DS</sub> = 20V, I <sub>D</sub> = 500mA, Note 1	0.55	0.93	S
<b>C<sub>iss</sub></b>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz		260	pF
<b>C<sub>oss</sub></b>			22	pF
<b>C<sub>rss</sub></b>			8	pF
<b>t<sub>d(on)</sub></b>	<b>Resistive Switching Times</b> V <sub>GS</sub> = 10V, V <sub>DS</sub> = 0.5 • V <sub>DSS</sub> , I <sub>D</sub> = 1A R <sub>G</sub> = 47Ω (External)		11	ns
<b>t<sub>r</sub></b>			19	ns
<b>t<sub>d(off)</sub></b>			40	ns
<b>t<sub>f</sub></b>			28	ns
<b>Q<sub>g(on)</sub></b>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 0.5 • V <sub>DSS</sub> , I <sub>D</sub> = 1A		7.8	nC
<b>Q<sub>gs</sub></b>			1.4	nC
<b>Q<sub>gd</sub></b>			4.1	nC
<b>R<sub>thJC</sub></b>			3.1	°C/W
<b>R<sub>thCS</sub></b>	(TO-220)	0.50		°C/W

**Source-Drain Diode**

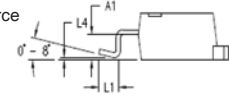
Symbol	Test Conditions (T <sub>J</sub> = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
<b>I<sub>S</sub></b>	V <sub>GS</sub> = 0V			750 mA
<b>I<sub>SM</sub></b>	Repetitive, Pulse Width Limited by T <sub>JM</sub>			3 A
<b>V<sub>SD</sub></b>	I <sub>F</sub> = I <sub>S</sub> , V <sub>GS</sub> = 0V, Note 1			1.5 V
<b>t<sub>rr</sub></b>	I <sub>F</sub> = I <sub>S</sub> , -di/dt = 100A/μs V <sub>R</sub> = 100V, V <sub>GS</sub> = 0V		710	ns

Note 1: Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

**TO-263AA Outline**

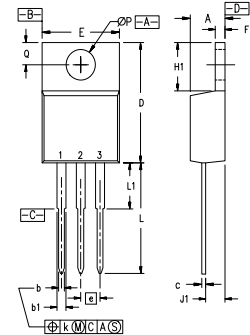


PIN: 1 - Gate  
2,4 - Source  
3 - Drain



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.160	.190	4.06	4.83
A1	.080	.110	2.03	2.79
b	.020	.039	0.51	0.99
b2	.045	.055	1.14	1.40
c	.016	.029	0.40	0.74
c2	.045	.055	1.14	1.40
D	.340	.380	8.64	9.65
D1	.315	.350	8.00	8.89
E	.380	.410	9.65	10.41
E1	.245	.320	6.22	8.13
e	.100 BSC		2.54 BSC	
L	.575	.625	14.61	15.88
L1	.090	.110	2.29	2.79
L2	.040	.055	1.02	1.40
L3	.050	.070	1.27	1.78
L4	0	.005	0	0.13

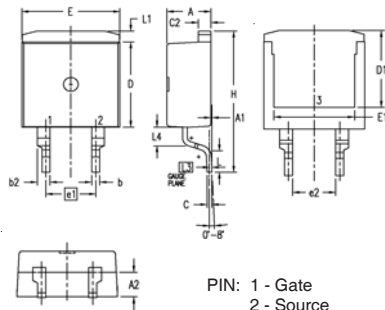
**TO-220AB Outline**



Pins: 1 - Gate  
2 - Drain  
3 - Source

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100 BSC		2.54 BSC	
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
∅P	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

**TO-263HV Outline**



PIN: 1 - Gate  
2 - Source  
3 - Drain

SYM	INCHES		MILLIMETER	
	MIN	MAX	MIN	MAX
A	.170	.185	4.30	4.70
A1	.000	.008	0.00	0.20
A2	.091	.098	2.30	2.50
b	.028	.035	0.70	0.90
b2	.046	.054	1.18	1.38
C	.018	.024	0.45	0.60
C2	.049	.055	1.25	1.40
D	.354	.370	9.00	9.40
D1	.311	.327	7.90	8.30
E	.386	.402	9.80	10.20
E1	.307	.323	7.80	8.20
e1	.200 BSC		5.08 BSC	
(e2)	.163	.174	4.13	4.43
H	.591	.614	15.00	15.60
L	.079	.102	2.00	2.60
L1	.039	.055	1.00	1.40
L3	.010 BSC		0.254 BSC	
(L4)	.071	.087	1.80	2.20

IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065 B1 6,683,344 6,727,585 7,005,734 B2 7,157,338B2  
by one or more of the following U.S. patents: 4,860,072 5,017,508 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405 B2 6,759,692 7,063,975 B2  
4,881,106 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,583,505 6,710,463 6,771,478 B2 7,071,537

Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$

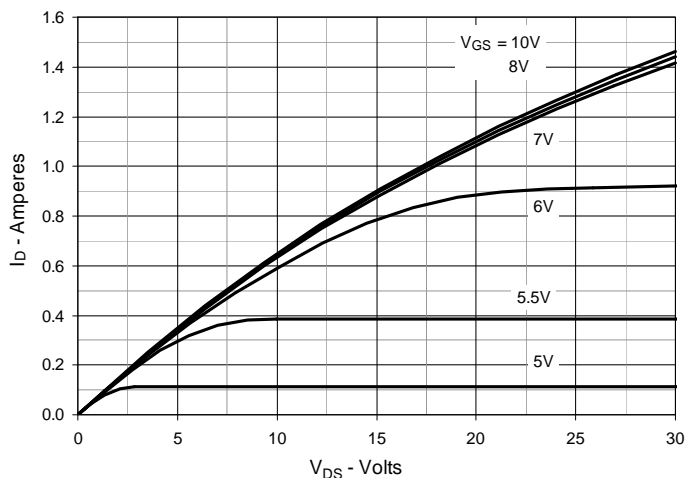


Fig. 2. Output Characteristics @  $T_J = 125^\circ\text{C}$

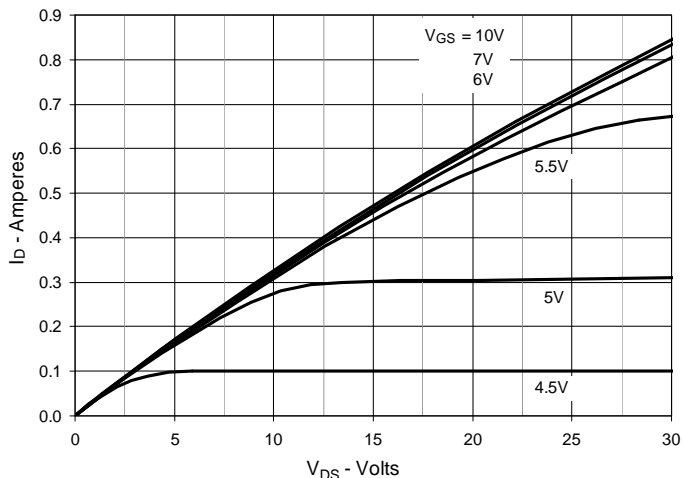


Fig. 3.  $R_{DS(on)}$  Normalized to  $I_D = 375\text{mA}$  Value vs. Junction Temperature

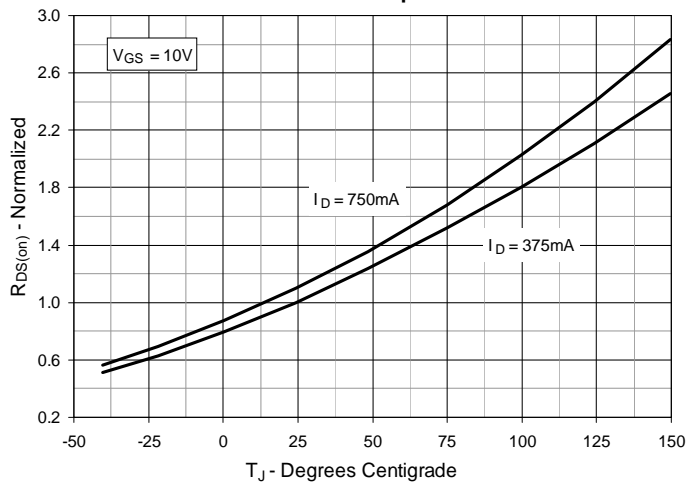


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 375\text{mA}$  Value vs. Drain Current

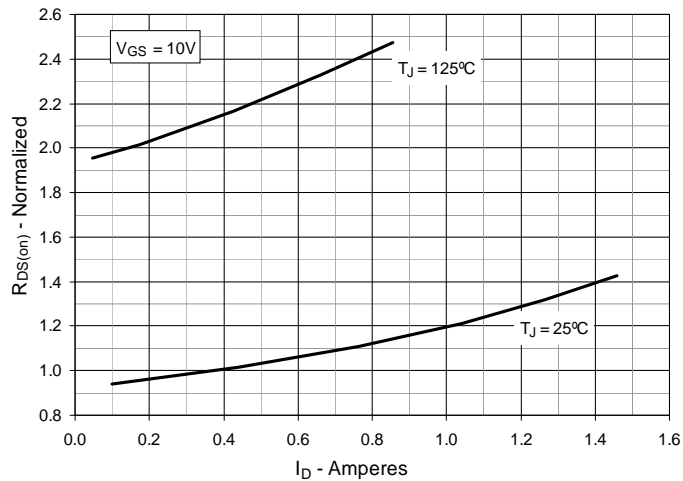


Fig. 5. Maximum Drain Current vs. Case Temperature

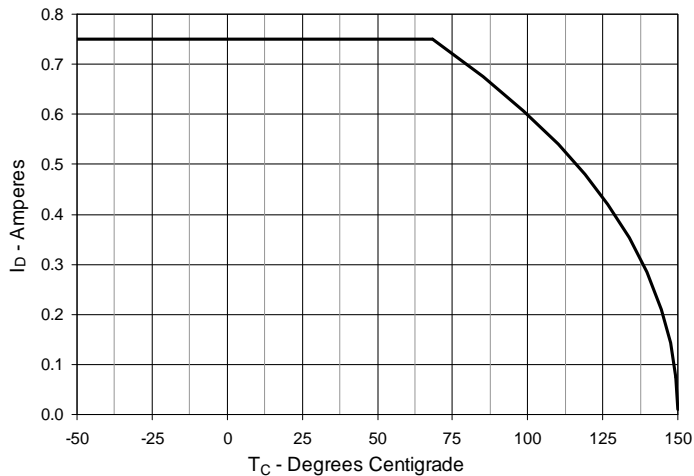
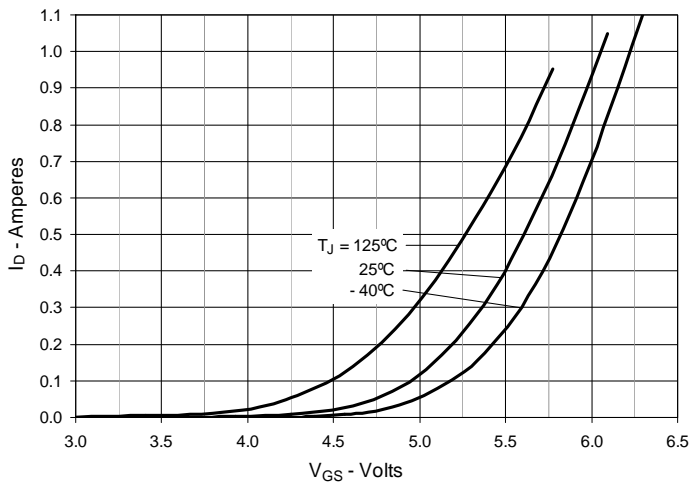
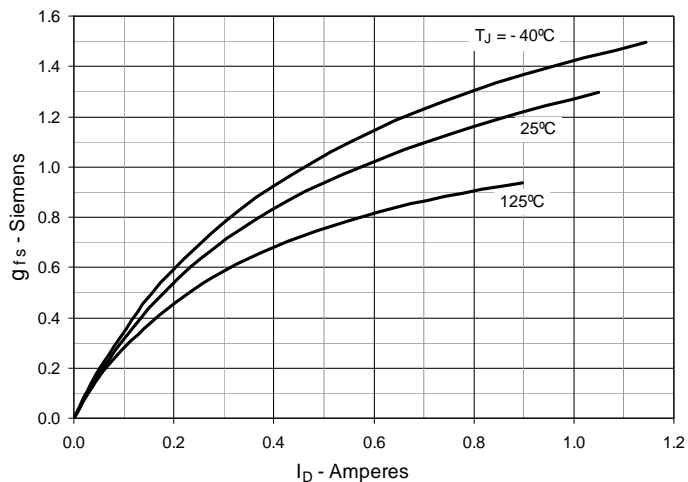


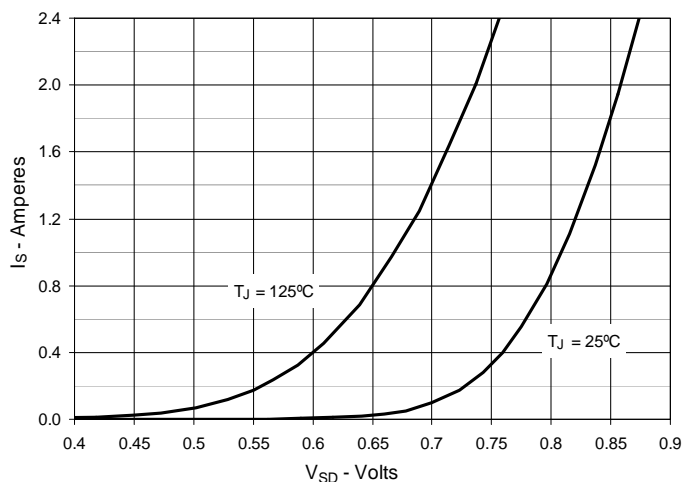
Fig. 6. Input Admittance



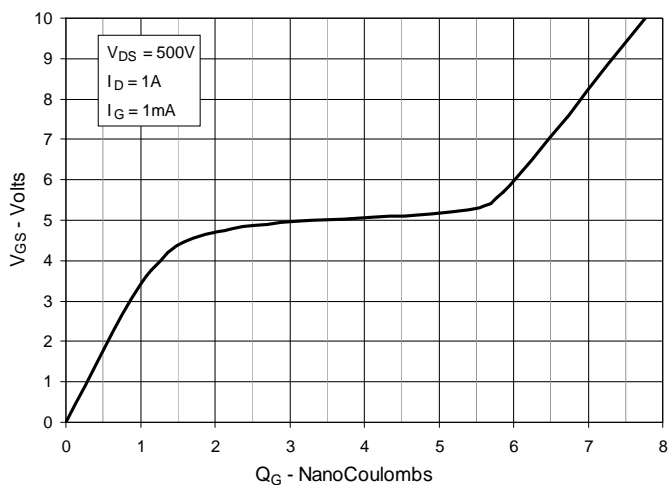
**Fig. 7. Transconductance**



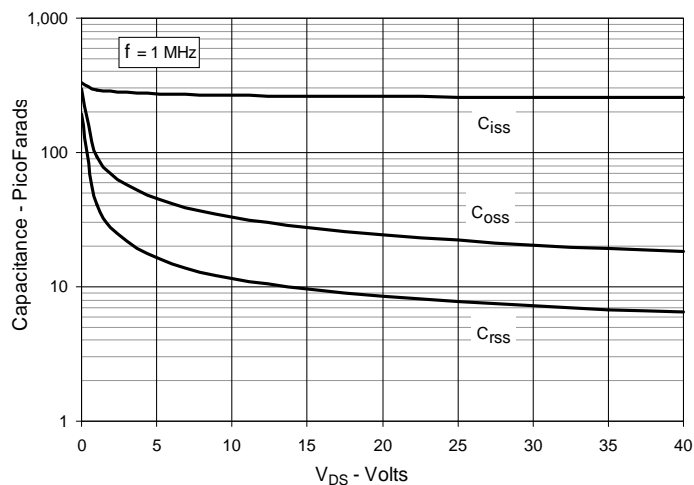
**Fig. 8. Forward Voltage Drop of Intrinsic Diode**



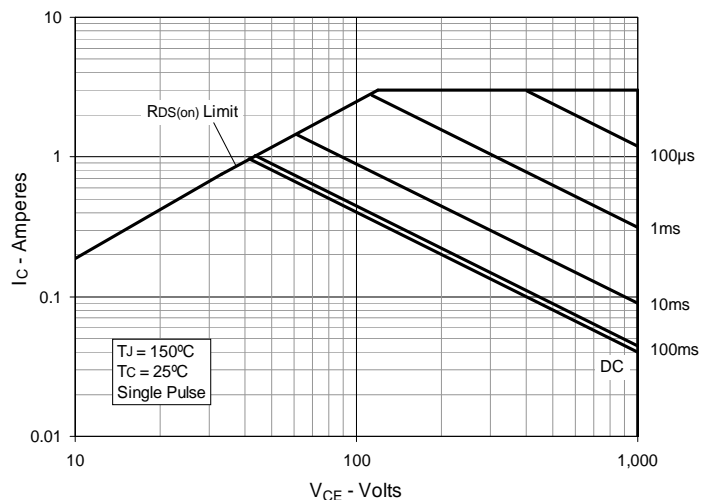
**Fig. 9. Gate Charge**



**Fig. 10. Capacitance**



**Fig. 11. Forward-Bias Safe Operating Area @  $T_C = 25^\circ\text{C}$**



**Fig. 12. Forward-Bias Safe Operating Area @  $T_C = 75^\circ\text{C}$**

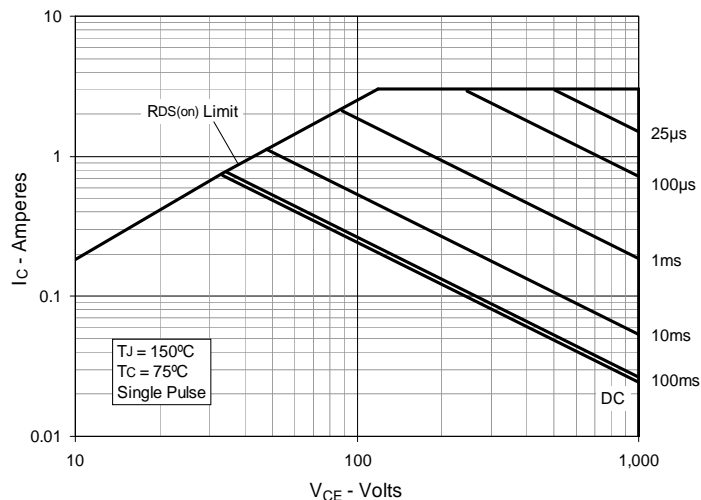
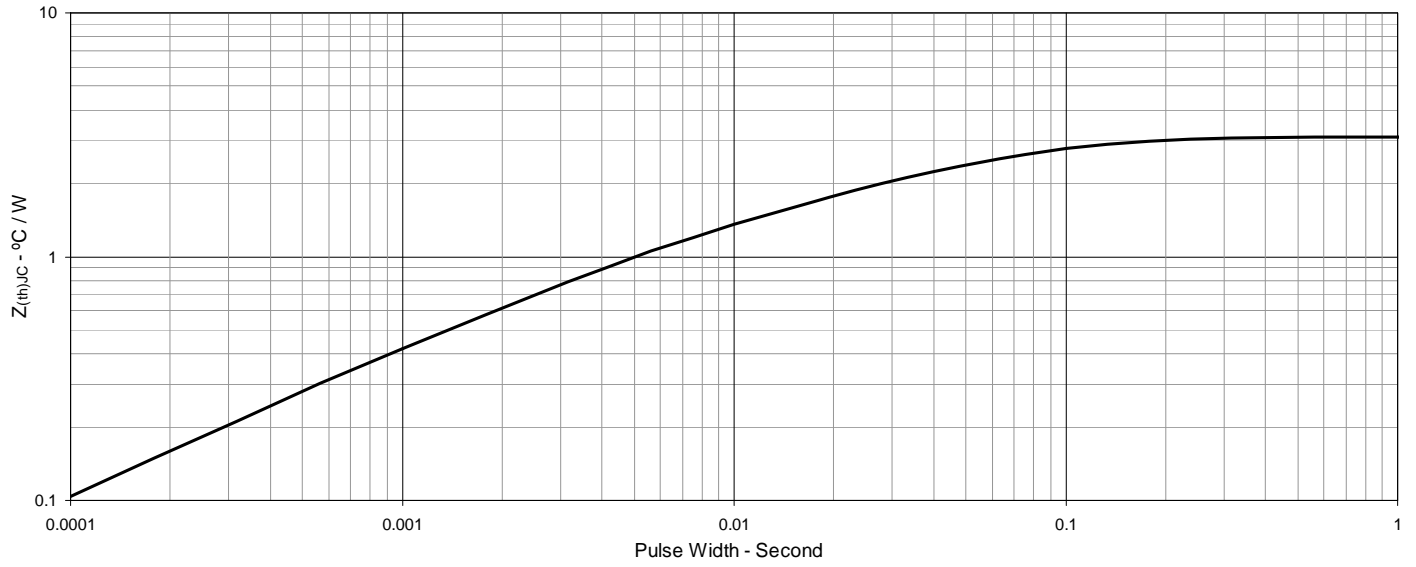


Fig. 13. Maximum Transient Thermal Impedance





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