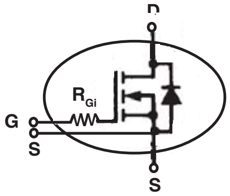
IXYS **Advance Technical Information**

LinearL2™ Power MOSFET w/ Extended FBSOA

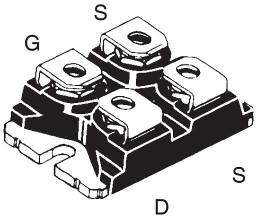
IXTN80N30L2

V_{DSS} = 300V
I_{D25} = 80A
$R_{DS(on)} \leq$ 38mΩ

N-Channel Enhancement Mode
Avalanche Rated



miniBLOC, SOT-227
E153432



G = Gate D = Drain
S = Source

Symbol	Test Conditions		Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C		300	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ		300	V
V_{GSS}	Continuous		±20	V
V_{GSM}	Transient		±30	V
I_{D25}	T_C = 25°C		80	A
I_{DM}	T_C = 25°C, Pulse Width Limited by T_{JM}		200	A
I_A	T_C = 25°C		80	A
E_{AS}	T_C = 25°C		3	J
P_D	T_C = 25°C		735	W
T_J			-55 ... +150	°C
T_{JM}			150	°C
T_{stg}			-55 ... +150	°C
V_{ISOL}	50/60 Hz, RMS	t = 1 minute	2500	V~
	$I_{ISOL} \leq$ 1mA	t = 1 second	3000	V~
M_d	Mounting Torque		1.5/13	Nm/lb.in
	Terminal Connection Torque		1.3/11.5	Nm/lb.in
Weight			30	g

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	 Typ.	 Max.	
BV_{DSS}	V_{GS} = 0V, I_D = 1mA	300			V
$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 3mA	2.5		4.5	V
I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V			±200	nA
I_{DSS}	V_{DS} = V_{DSS}, V_{GS} = 0V			10	µA
	T_J = 125°C			250	µA
$R_{DS(on)}$	V_{GS} = 10V, I_D = 0.5 • I_{D25}, Note 1		30	38	mΩ

Features

- International Standard Package
- miniBLOC, with Aluminium Nitride Isolation
- Isolation Voltage 2500V~
- High Current Handling Capability
- Avalanche Rated
- Low $R_{DS(on)}$
- Designed for Linear Operation
- Guaranteed FBSOA at 75°C

Advantages

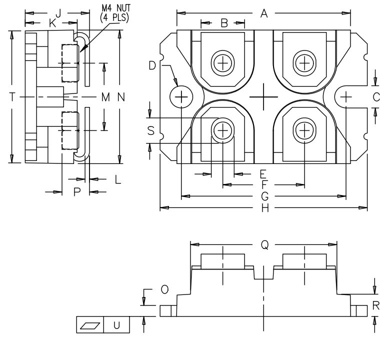
- Easy to Mount
- Space Savings
- High Power Density

Applications

- Solid State Circuit Breakers
- Soft Start Controls
- Linear Amplifiers
- Programmable Loads
- Current Regulators

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
g_{fs}	V_{DS} = 10V, I_D = 0.5 • I_{D25}, Note 1	24	36	48	S
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		19.1		nF
C_{oss}			1760		pF
C_{rss}			490		pF
R_{Gi}	Integrated Gate Input Resistor		0.88		Ω
$t_{d(on)}$	**Resistive Switching Times** V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25} R_G = 1Ω (External)		40		ns
t_r			180		ns
$t_{d(off)}$			174		ns
t_f			67		ns
$Q_{g(on)}$	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25}		660		nC
Q_{gs}			107		nC
Q_{gd}			364		nC
R_{thJC}				0.17	°C/W
R_{thCS}			0.05		°C/W

SOT-227B (IXTN) Outline



(M4 screws (4x) supplied)

SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	1.240	1.255	31.50	31.88
B	.307	.323	7.80	8.20
C	.161	.169	4.09	4.29
D	.161	.169	4.09	4.29
E	.161	.169	4.09	4.29
F	.587	.595	14.91	15.11
G	1.186	1.193	30.12	30.30
H	1.496	1.505	38.00	38.23
J	.460	.481	11.68	12.22
K	.351	.378	8.92	9.60
L	.030	.033	0.76	0.84
M	.496	.506	12.60	12.85
N	.990	1.001	25.15	25.42
O	.078	.084	1.98	2.13
P	.195	.235	4.95	5.97
Q	1.045	1.059	26.54	26.90
R	.155	.174	3.94	4.42
S	.186	.191	4.72	4.85
T	.968	.987	24.59	25.07
U	-.002	.004	-0.05	0.1

Safe Operating Area Specification

Symbol	Test Conditions	Characteristic Values Min.	Typ.	Max.	
SOA	V_{DS} = 300V, I_D = 1.47A, T_C = 75°C, Tp = 5s	440			W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max	
I_S	V_{GS} = 0V			80	A
I_{SM}	Repetitive, pulse Width Limited by T_{JM}			320	A
V_{SD}	I_F = I_S, V_{GS} = 0V, Note 1			1.4	V
t_{rr}	I_F = 40A, -di/dt = 100A/μs V_R = 100V		485		ns
Q_{RM}			10		μC
I_{RM}			42		A

Note 1. Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

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IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065B1 6,683,344 6,727,585 7,005,734B2 7,157,338B2 4,860,072 5,017,508 5,063,307 5,381,025 6,259,123B1 6,534,343 6,710,405B2 6,759,692 7,063,975B2 4,881,106 5,034,796 5,187,117 5,486,715 6,306,728B1 6,583,505 6,710,463 6,771,478B2 7,071,537

Fig. 1. Output Characteristics @ $T_J = 25^oC$

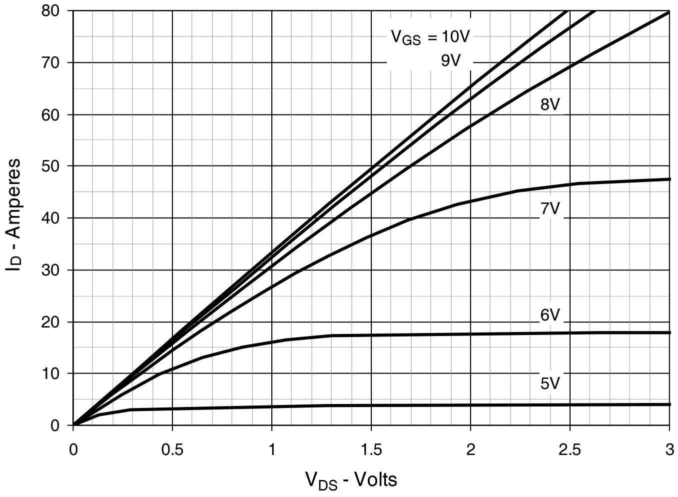


Fig. 2. Extended Output Characteristics @ $T_J = 25^oC$

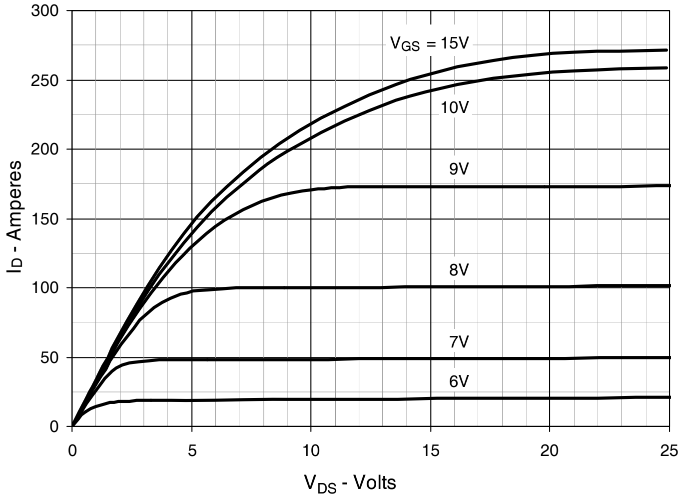


Fig. 3. Output Characteristics @ $T_J = 125^oC$

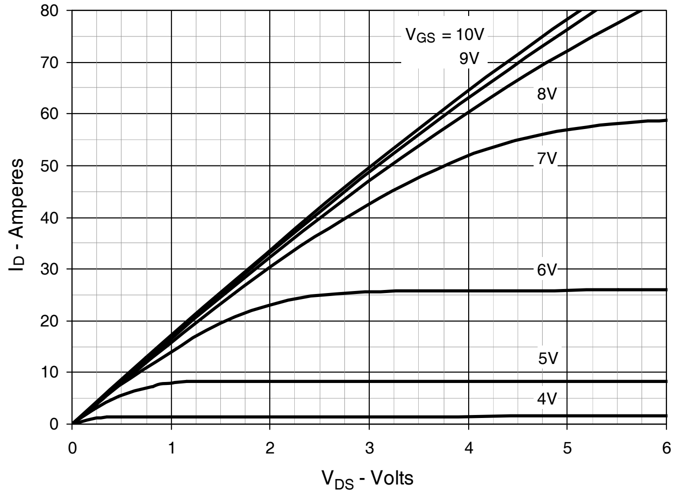


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 40A Value vs. Junction Temperature

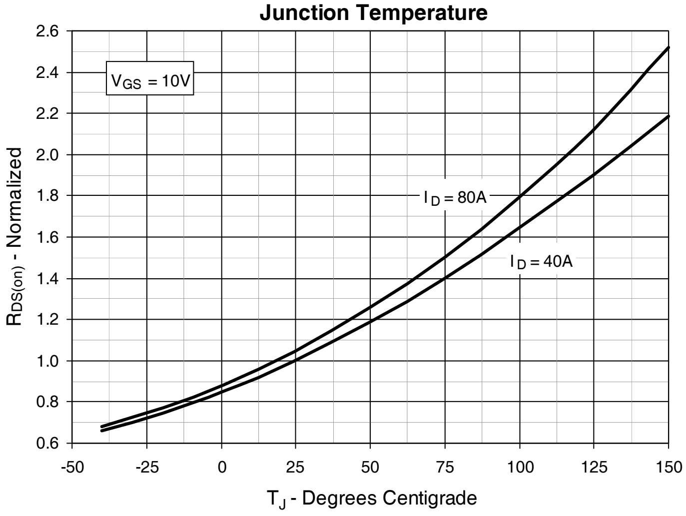


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 40A Value vs. Drain Current

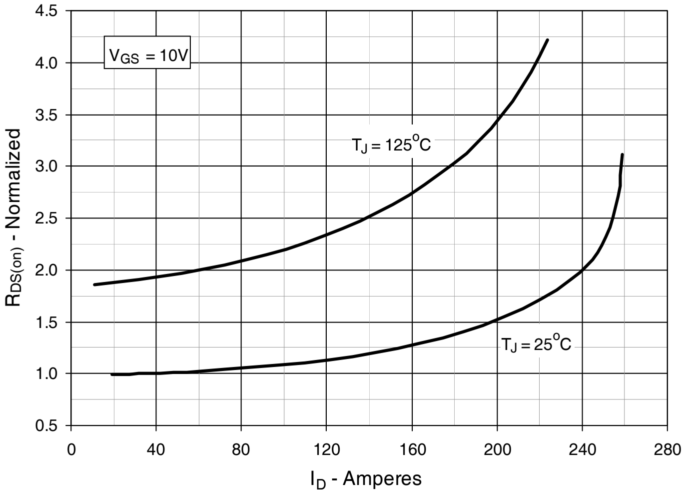


Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature

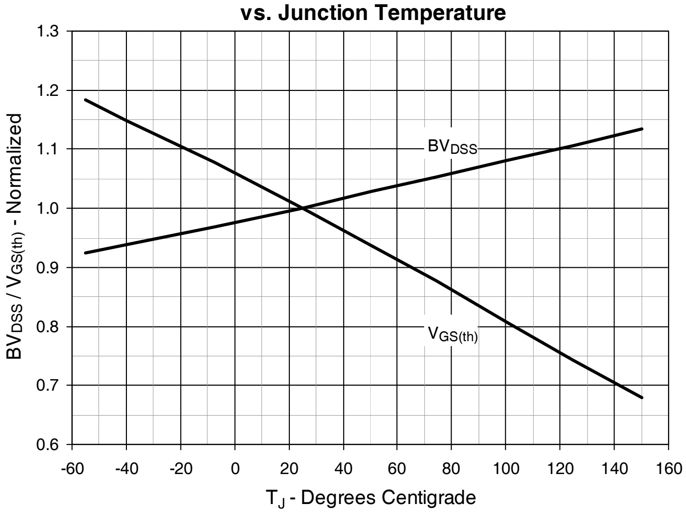


Fig. 7. Maximum Drain Current vs. Case Temperature

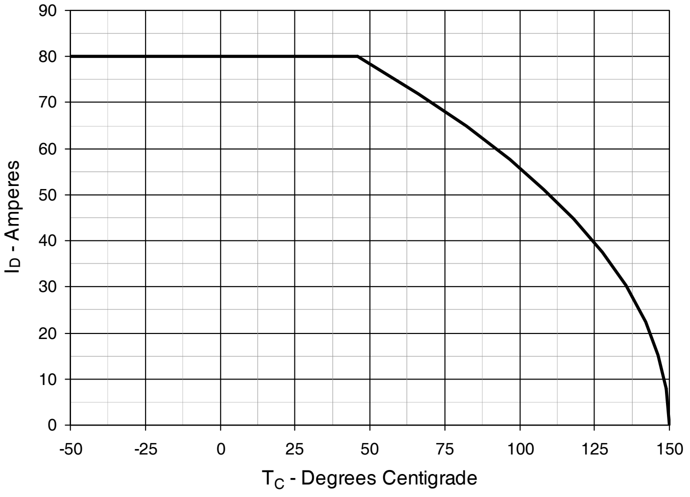


Fig. 8. Input Admittance

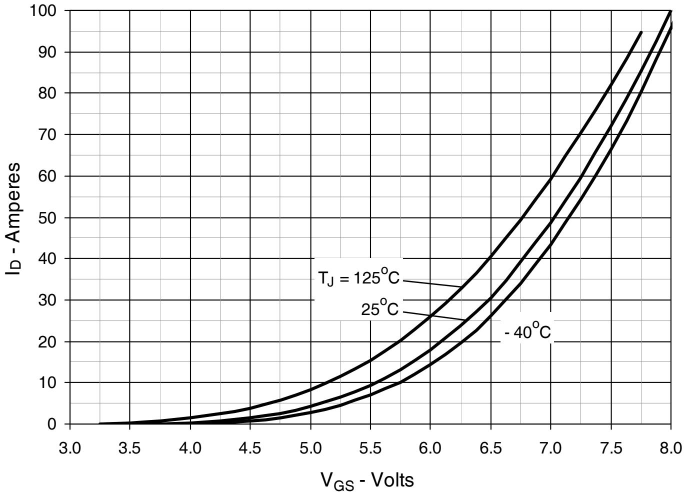


Fig. 9. Transconductance

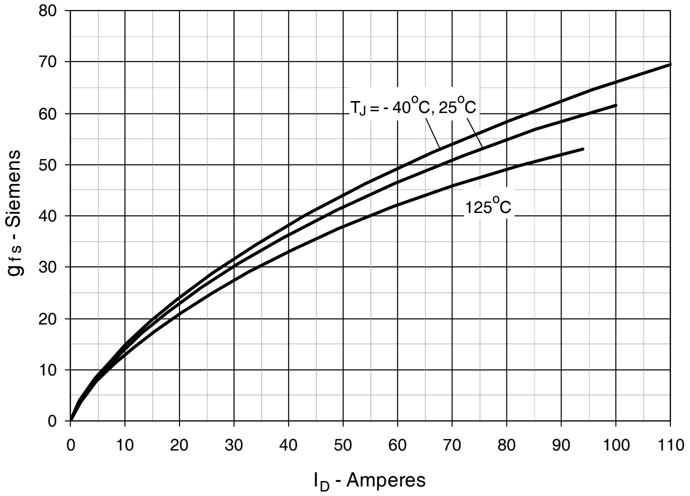


Fig. 10. Forward Voltage Drop of Intrinsic Diode

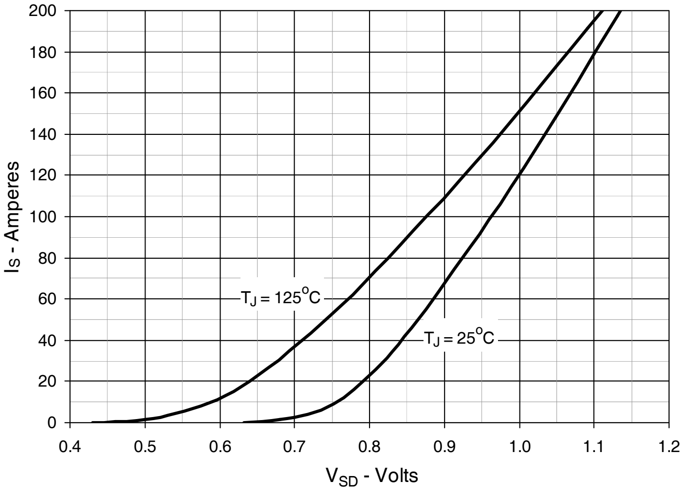


Fig. 11. Gate Charge

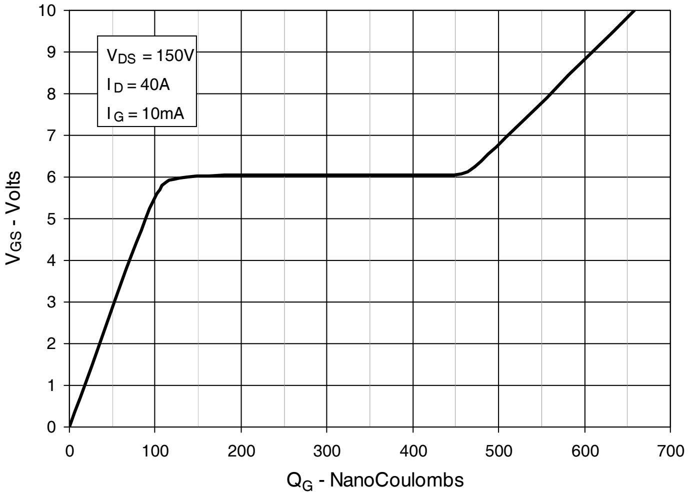


Fig. 12. Capacitance

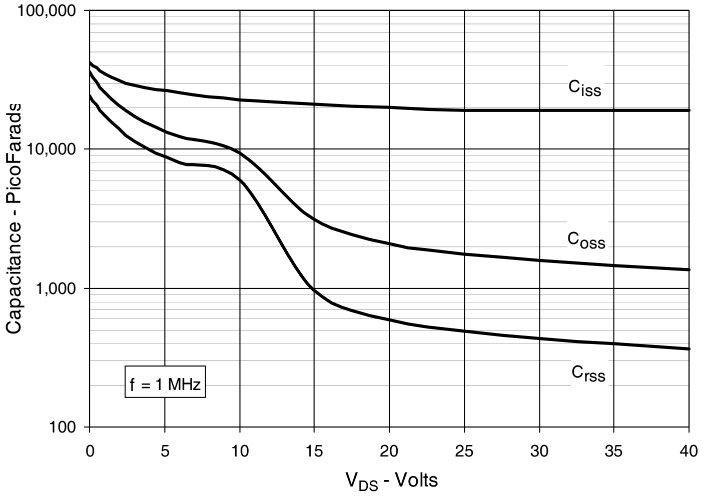


Fig. 13. Forward-Bias Safe Operating Area @ $T_C = 25^oC$

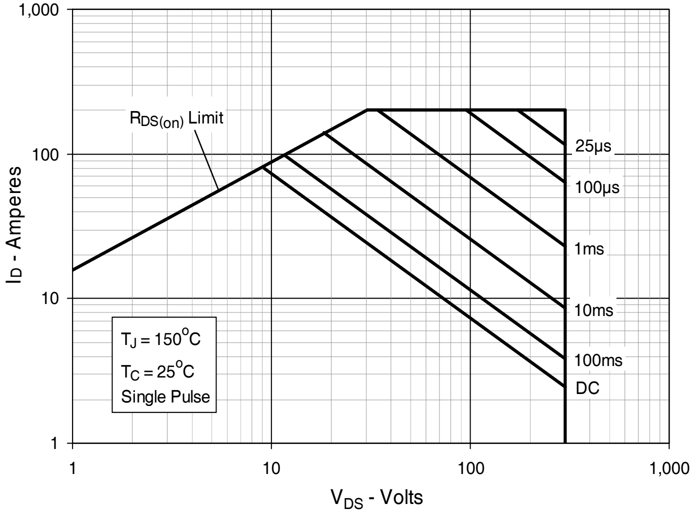


Fig. 14. Forward-Bias Safe Operating Area @ $T_C = 75^oC$

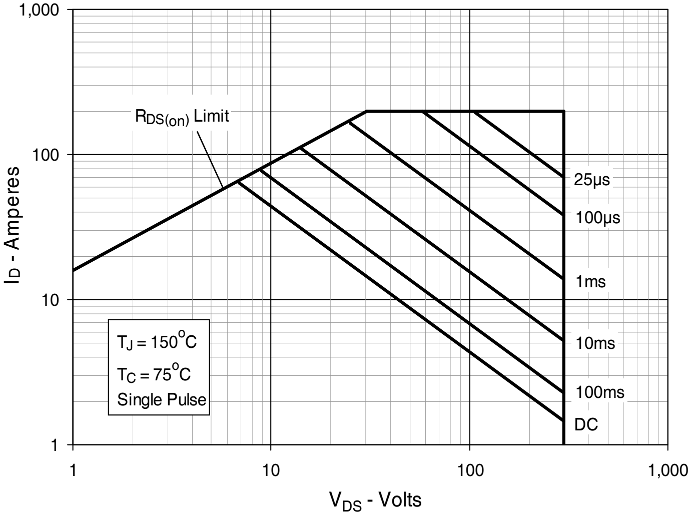
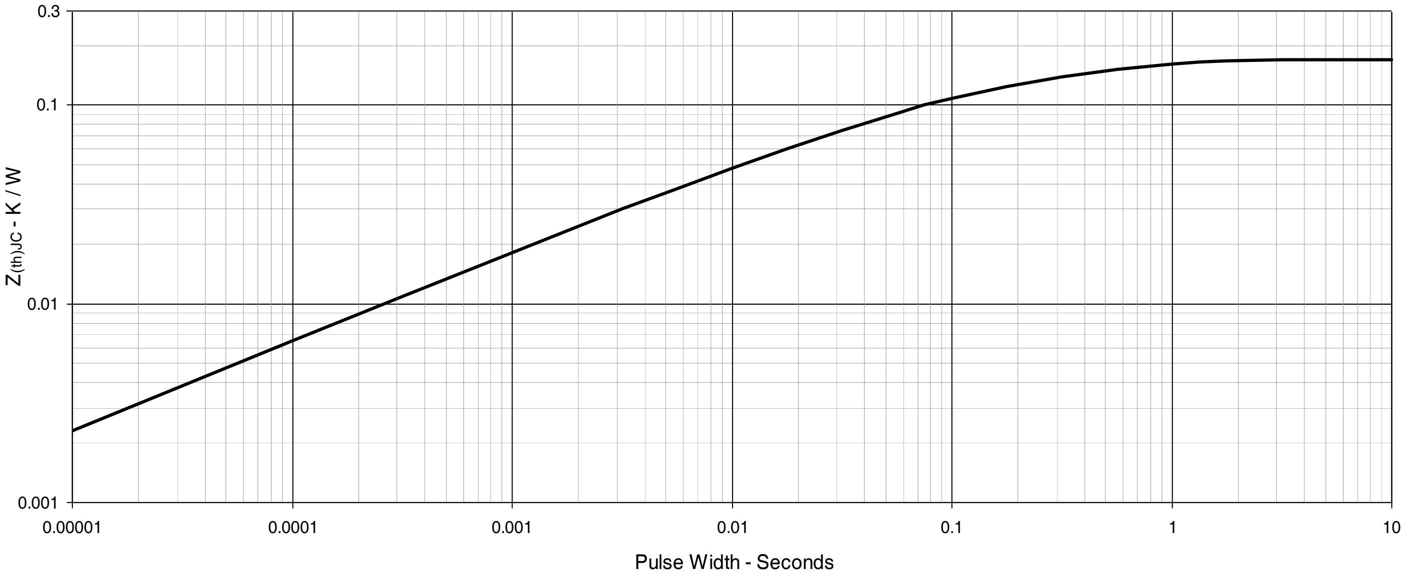


Fig. 15. Maximum Transient Thermal Impedance



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