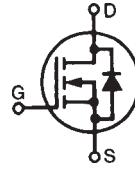


Polar™ HiPerFET™
Power MOSFET

IXFR18N90P

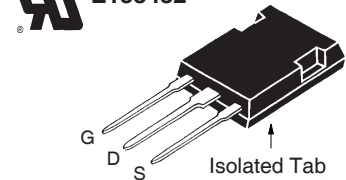
N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Diode



$V_{DSS} = 900V$
 $I_{D25} = 10.5A$
 $R_{DS(on)} \leq 660m\Omega$
 $t_{rr} \leq 300ns$

| Symbol | Test Conditions | Maximum Ratings | |
|---------------|--|-----------------|------------|
| V_{DSS} | $T_J = 25^\circ C$ to $150^\circ C$ | 900 | V |
| V_{DGR} | $T_J = 25^\circ C$ to $150^\circ C$, $R_{GS} = 1M\Omega$ | 900 | V |
| V_{GSS} | Continuous | ± 30 | V |
| V_{GSM} | Transient | ± 40 | V |
| I_{D25} | $T_C = 25^\circ C$ | 10.5 | A |
| I_{DM} | $T_C = 25^\circ C$, Pulse Width Limited by T_{JM} | 36 | A |
| I_A | $T_C = 25^\circ C$ | 9 | A |
| E_{AS} | $T_C = 25^\circ C$ | 800 | mJ |
| dv/dt | $I_S \leq I_{DM}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ C$ | 15 | V/ns |
| P_D | $T_C = 25^\circ C$ | 200 | W |
| T_J | | -55 ... +150 | $^\circ C$ |
| T_{JM} | | 150 | $^\circ C$ |
| T_{stg} | | -55 ... +150 | $^\circ C$ |
| T_L | Maximum Lead Temperature for Soldering | 300 | $^\circ C$ |
| T_{SOLD} | Plastic body for 10s | 260 | $^\circ C$ |
| V_{ISOL} | 50/60 Hz, RMS, 1 minute | 2500 | V~ |
| F_C | Mounting force | 20..120/4.5..27 | N/lb. |
| Weight | | 5 | g |

ISOPLUS247™
E153432



G = Gate D = Drain
S = Source

Features

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
- Isolated Mounting Surface
- 2500V~ Electrical Isolation
- Fast Intrinsic Diode
- Avalanche Rated
- Low Package Inductance

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- DC-DC Converters
- Switch-Mode and Resonant-Mode Power Supplies
- Leaser Drivers
- DC Choppers
- AC and DC Motor Drives

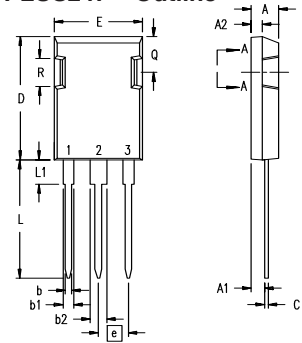
| Symbol | Test Conditions ($T_J = 25^\circ C$, unless otherwise specified) | Characteristic Values | | |
|--------------|---|-----------------------|------|----------------------|
| | | Min. | Typ. | Max. |
| BV_{DSS} | $V_{GS} = 0V$, $I_D = 1mA$ | 900 | | V |
| $V_{GS(th)}$ | $V_{DS} = V_{GS}$, $I_D = 1mA$ | 3.5 | | 6.5 V |
| I_{GSS} | $V_{GS} = \pm 30V$, $V_{DS} = 0V$ | | | ± 100 nA |
| I_{DSS} | $V_{DS} = V_{DSS}$ $V_{GS} = 0V$ | | | 25 μA 1.5 mA |
| $R_{DS(on)}$ | $V_{GS} = 10V$, $I_D = 9A$, Note 1 | | | 660 m Ω |

| Symbol | Test Conditions ($T_J = 25^\circ\text{C}$ unless otherwise specified) | Characteristic Values | | |
|--------------|--|-----------------------|------|--------------------|
| | | Min. | Typ. | Max. |
| g_{fs} | $V_{DS} = 20\text{V}, I_D = 9\text{A}$, Note 1 | 6 | 10 | S |
| R_{Gi} | Gate input resistance | | 1.2 | Ω |
| C_{iss} | $V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1\text{MHz}$ | | 5230 | pF |
| C_{oss} | | | 366 | pF |
| C_{rss} | | | 53 | pF |
| $t_{d(on)}$ | Resistive Switching Times $V_{GS} = 10\text{V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 9\text{A}$ $R_G = 2\Omega$ (External) | | 40 | ns |
| t_r | | | 33 | ns |
| $t_{d(off)}$ | | | 60 | ns |
| t_f | | | 44 | ns |
| $Q_{g(on)}$ | $V_{GS} = 10\text{V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 9\text{A}$ | | 97 | nC |
| Q_{gs} | | | 30 | nC |
| Q_{gd} | | | 40 | nC |
| R_{thJC} | | | 0.62 | $^\circ\text{C/W}$ |
| R_{thCS} | | 0.15 | | $^\circ\text{C/W}$ |

| Symbol | Test Conditions ($T_J = 25^\circ\text{C}$ unless otherwise specified) | Characteristic Values | | |
|----------|--|-----------------------|------|---------------|
| | | Min. | Typ. | Max. |
| I_S | $V_{GS} = 0\text{V}$ | | | 18 A |
| I_{SM} | Repetitive, pulse width limited by T_{JM} | | | 72 A |
| V_{SD} | $I_F = I_S, V_{GS} = 0\text{V}$, Note 1 | | | 1.5 V |
| t_{rr} | $I_F = 9\text{A}, -di/dt = 100\text{A}/\mu\text{s}$ $V_R = 100\text{V}, V_{GS} = 0\text{V}$ | | | 300 ns |
| Q_{RM} | | | 1.0 | μC |
| I_{RM} | | | 10.8 | A |

Note 1: Pulse test, $t \leq 300\mu\text{s}$; duty cycle, $d \leq 2\%$.

ISOPLUS247™ Outline



- Terminals: 1 - Gate
2 - Drain (Collector)
3 - Source (Emitter)
4 - Drain (Collector)

| Dim. | Millimeter | | Inches | |
|----------------|------------|-------|----------|-------|
| | Min. | Max. | Min. | Max. |
| A | 4.83 | 5.21 | .190 | .205 |
| A ₁ | 2.29 | 2.54 | .090 | .100 |
| A ₂ | 1.91 | 2.16 | .075 | .085 |
| b | 1.14 | 1.40 | .045 | .055 |
| b ₁ | 1.91 | 2.13 | .075 | .084 |
| b ₂ | 2.92 | 3.12 | .115 | .123 |
| C | 0.61 | 0.80 | .024 | .031 |
| D | 20.80 | 21.34 | .819 | .840 |
| E | 15.75 | 16.13 | .620 | .635 |
| e | 5.45 BSC | | .215 BSC | |
| L | 19.81 | 20.32 | .780 | .800 |
| L1 | 3.81 | 4.32 | .150 | .170 |
| Q | 5.59 | 6.20 | .220 | 0.244 |
| R | 4.32 | 4.83 | .170 | .190 |

IXYS Reserves the Right to Rchange Limits, Test Conditions, and Dimensions.

| | | | | | | | | | | |
|--|-----------|-----------|-----------|-----------|--------------|--------------|--------------|--------------|--------------|-------------|
| IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: | 4,835,592 | 4,931,844 | 5,049,961 | 5,237,481 | 6,162,665 | 6,404,065 B1 | 6,683,344 | 6,727,585 | 7,005,734 B2 | 7,157,338B2 |
| | 4,850,072 | 5,017,508 | 5,063,307 | 5,381,025 | 6,259,123 B1 | 6,534,343 | 6,710,405 B2 | 6,759,692 | 7,063,975 B2 | |
| | 4,881,106 | 5,034,796 | 5,187,117 | 5,486,715 | 6,306,728 B1 | 6,583,505 | 6,710,463 | 6,771,478 B2 | 7,071,537 | |

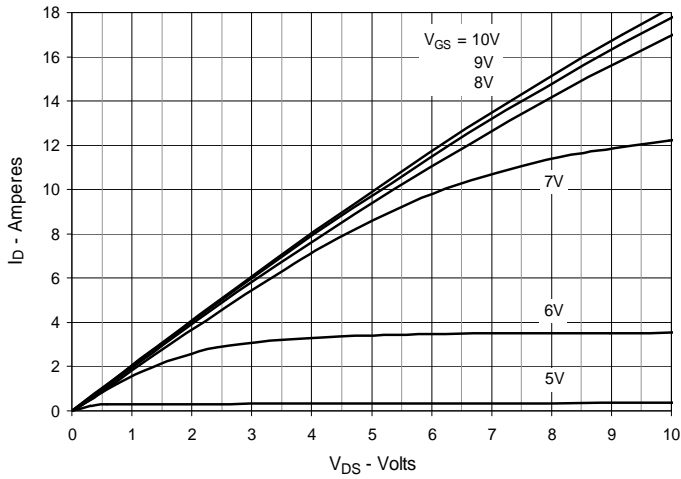
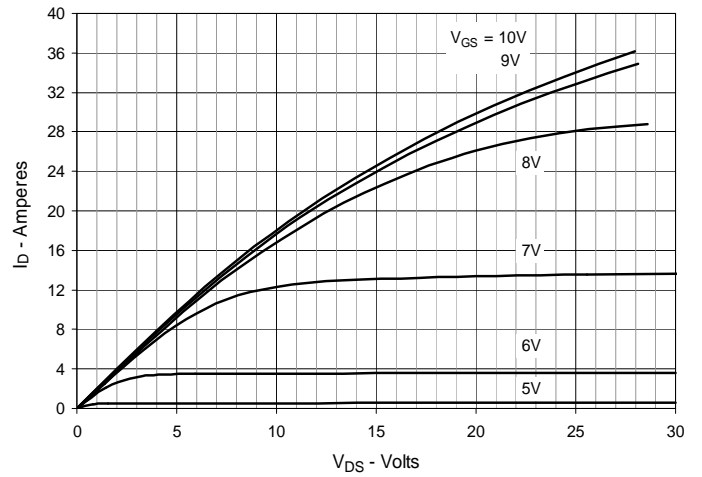
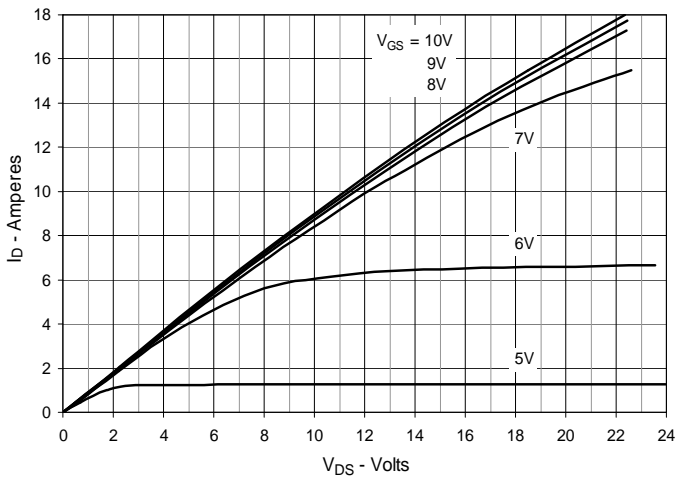
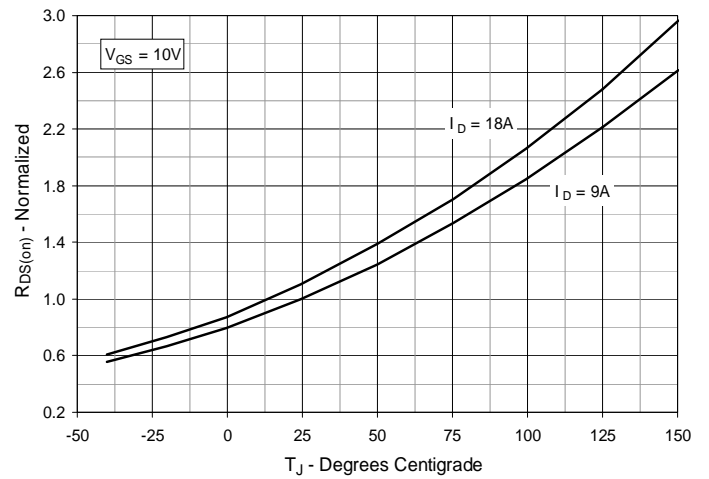
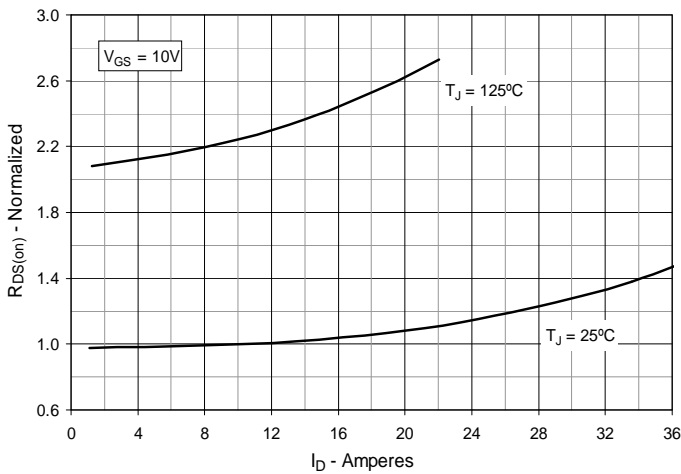
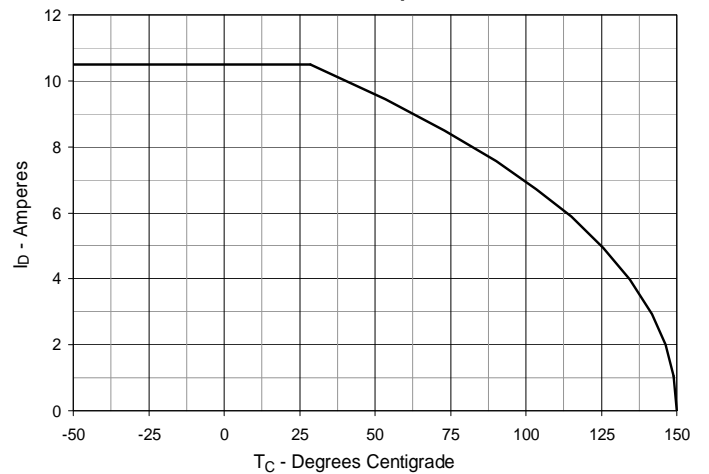
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$

Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 9\text{A}$ Value vs. Junction Temperature

Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 9\text{A}$ Value vs. Drain Current

Fig. 6. Maximum Drain Current vs. Case Temperature


Fig. 7. Input Admittance

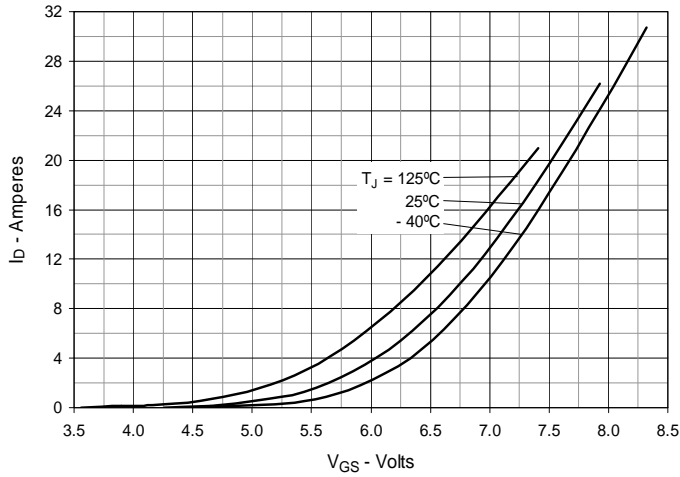


Fig. 8. Transconductance

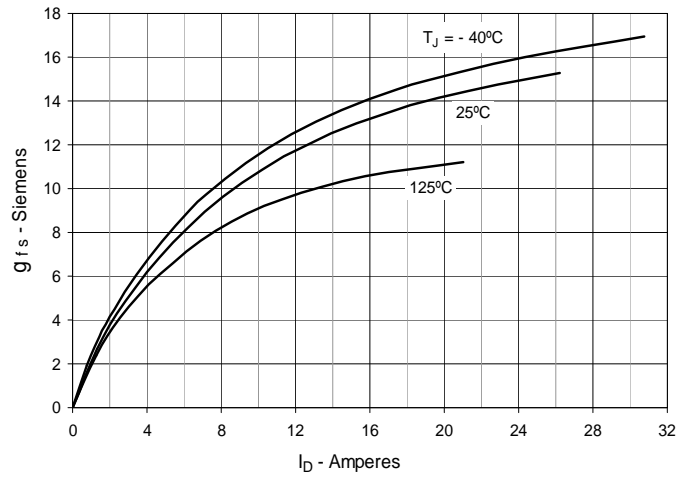


Fig. 9. Forward Voltage Drop of Intrinsic Diode

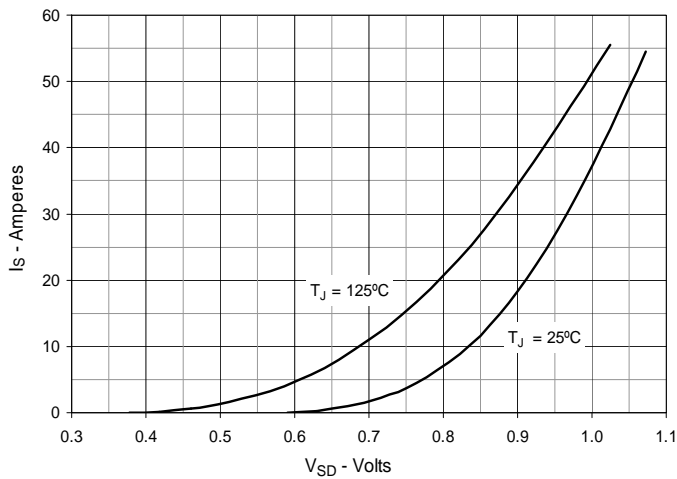


Fig. 10. Gate Charge

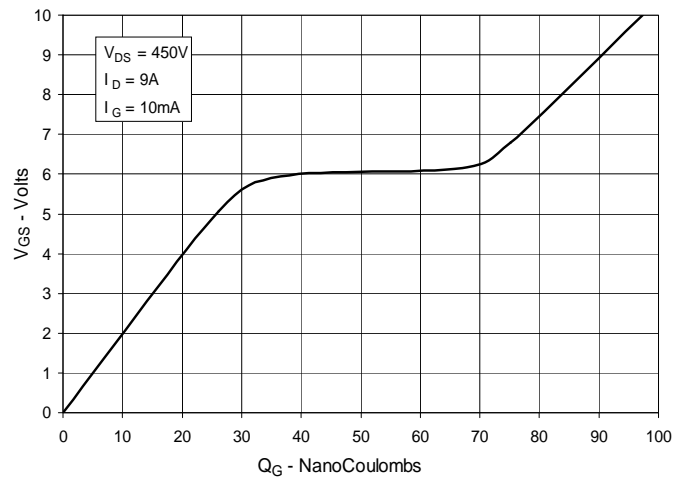


Fig. 11. Capacitance

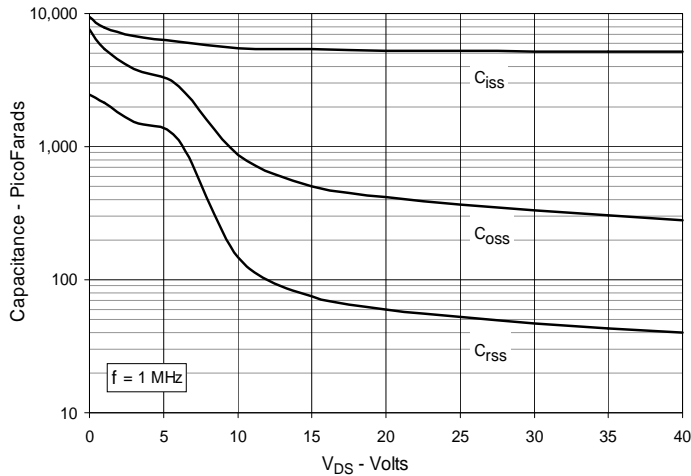
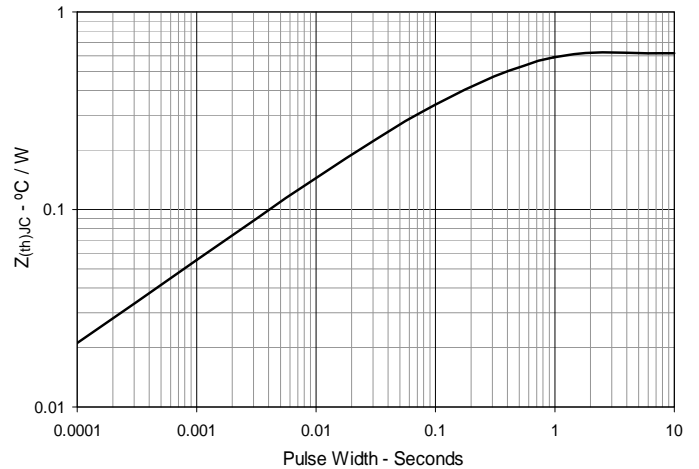


Fig. 12. Maximum Transient Thermal Impedance





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