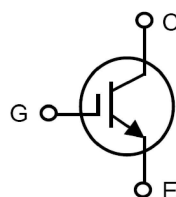


XPT™ 650V GenX5™ IGBT

IXYH90N65A5

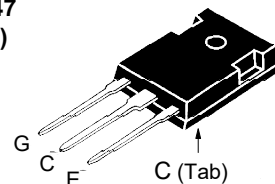
Extreme Light Punch Through
IGBT for up to 10kHz Switching



$$\begin{aligned} V_{CES} &= 650V \\ I_{C110} &= 90A \\ V_{CE(sat)} &\leq 1.35V \\ t_{fi(typ)} &= 220ns \end{aligned}$$

| Symbol | Test Conditions | Maximum Ratings | |
|-------------------------------|---|---|------------------|
| V_{CES} | $T_J = 25^\circ\text{C to } 175^\circ\text{C}$ | 650 | V |
| V_{CGR} | $T_J = 25^\circ\text{C to } 175^\circ\text{C}, R_{GE} = 1M\Omega$ | 650 | V |
| V_{GES} | Continuous | ± 20 | V |
| V_{GEM} | Transient | ± 30 | V |
| I_{C25} | $T_C = 25^\circ\text{C}$ (Chip Capability) | 220 | A |
| I_{LRMS} | Terminal Current Limit | 160 | A |
| I_{C110} | $T_C = 110^\circ\text{C}$ | 90 | A |
| I_{CM} | $T_C = 25^\circ\text{C}, 1\text{ms}$ | 600 | A |
| SSOA (RBSOA) | $V_{GE} = 15V, T_{VJ} = 150^\circ\text{C}, R_G = 5\Omega$ Clamped Inductive Load | $I_{CM} = 180$ $V_{CE} \leq V_{CES}$ | A |
| P_C | $T_C = 25^\circ\text{C}$ | 650 | W |
| T_J | | -55 ... +175 | $^\circ\text{C}$ |
| T_{JM} | | 175 | $^\circ\text{C}$ |
| T_{stg} | | -55 ... +175 | $^\circ\text{C}$ |
| T_L | Maximum Lead Temperature for Soldering 1.6 mm (0.062 in.) from Case for 10s | 300 | $^\circ\text{C}$ |
| M_d | Mounting Torque | 1.13 / 10 | Nm/lb.in |
| Weight | | 6 | g |

TO-247
(IXYH)



G = Gate C = Collector
E = Emitter Tab = Collector

Features

- Optimized for Low Frequency High Current Switching
- High Surge Current Capability
- Square RBSOA
- International Standard Package

Advantages

- High Power Density
- Low Gate Drive Requirement

Applications

- Power Inverters
- UPS
- Motor Drives
- SMPS
- PFC Circuits
- Battery Chargers
- Welding Machines
- Lamp Ballasts

| Symbol | Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified) | Characteristic Values | | |
|---------------|---|-----------------------|------|--------------------------------------|
| | | Min. | Typ. | Max. |
| BV_{CES} | $I_C = 250\mu\text{A}, V_{GE} = 0V$ | 650 | | V |
| $V_{GE(th)}$ | $I_C = 250\mu\text{A}, V_{CE} = V_{GE}$ | 3.7 | | 5.8 V |
| I_{CES} | $V_{CE} = V_{CES}, V_{GE} = 0V$ $T_J = 150^\circ\text{C}$ | | | 5 μA 500 μA |
| I_{GES} | $V_{CE} = 0V, V_{GE} = \pm 20V$ | | | ± 100 nA |
| $V_{CE(sat)}$ | $I_C = 60A, V_{GE} = 15V, \text{Note 1}$ $T_J = 150^\circ\text{C}$ | 1.22 1.30 | | 1.35 V V |

| Symbol Test Conditions ($T_J = 25^\circ\text{C}$ Unless Otherwise Specified) | Characteristic Values | | |
|--|---|------|-------------------------|
| | Min. | Typ. | Max. |
| g_{fs} $I_C = 60\text{A}, V_{CE} = 10\text{V}$, Note 1 | 40 | 68 | S |
| C_{ies} C_{oes} C_{res} | $V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$ | 4040 | pF |
| | | 200 | pF |
| | | 150 | pF |
| $Q_{g(on)}$ Q_{ge} Q_{gc} | $I_C = 90\text{A}, V_{GE} = 15\text{V}, V_{CE} = 0.5 \cdot V_{CES}$ | 260 | nC |
| | | 33 | nC |
| | | 128 | nC |
| $t_{d(on)}$ t_{ri} E_{on} $t_{d(off)}$ t_{fi} E_{off} | Inductive load, $T_J = 25^\circ\text{C}$ $I_C = 50\text{A}, V_{GE} = 15\text{V}$ $V_{CE} = 400\text{V}, R_G = 5\Omega$ Note 2 | 40 | ns |
| | | 46 | ns |
| | | 1.3 | mJ |
| | | 420 | ns |
| | | 220 | ns |
| | | 3.4 | mJ |
| $t_{d(on)}$ t_{ri} E_{on} $t_{d(off)}$ t_{fi} E_{off} | Inductive load, $T_J = 150^\circ\text{C}$ $I_C = 50\text{A}, V_{GE} = 15\text{V}$ $V_{CE} = 400\text{V}, R_G = 5\Omega$ Note 2 | 24 | ns |
| | | 44 | ns |
| | | 2.8 | mJ |
| | | 380 | ns |
| | | 360 | ns |
| | | 5.0 | mJ |
| R_{thJC} | | | 0.23 $^\circ\text{C/W}$ |
| R_{thCS} | 0.21 | | $^\circ\text{C/W}$ |

Notes:

1. Pulse test, $t \leq 300\mu\text{s}$, duty cycle, $d \leq 2\%$.
2. Switching times & energy losses may increase for higher V_{CE} (clamp), T_J or R_G .

Littelfuse reserves the right to change limits, test conditions, and dimensions.

| | | | | | | | | | | |
|---|-----------|-----------|-----------|-----------|--------------|--------------|--------------|--------------|--------------|-------------|
| IXYS MOSFETs and IGBTs are covered | 4,835,592 | 4,931,844 | 5,049,961 | 5,237,481 | 6,162,665 | 6,404,065 B1 | 6,683,344 | 6,727,585 | 7,005,734 B2 | 7,157,338B2 |
| by one or more of the following U.S. patents: | 4,860,072 | 5,017,508 | 5,063,307 | 5,381,025 | 6,259,123 B1 | 6,534,343 | 6,710,405 B2 | 6,759,692 | 7,063,975 B2 | |
| | 4,881,106 | 5,034,796 | 5,187,117 | 5,486,715 | 6,306,728 B1 | 6,583,505 | 6,710,463 | 6,771,478 B2 | 7,071,537 | |

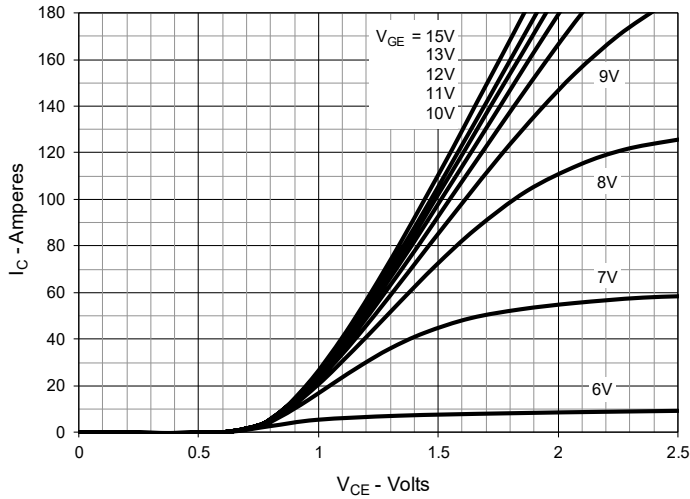
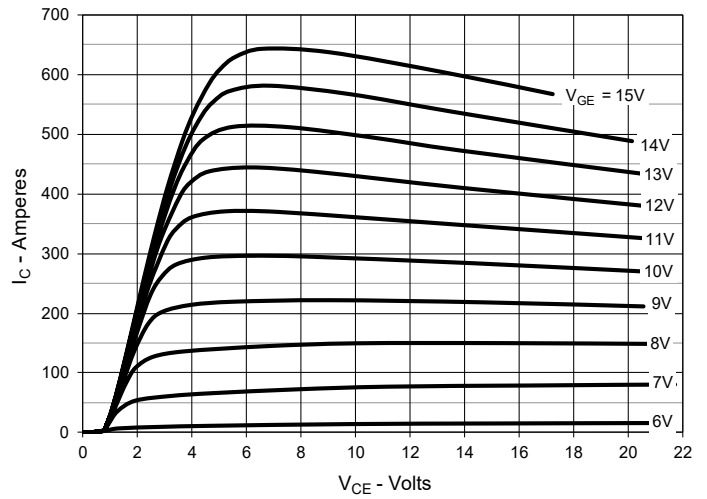
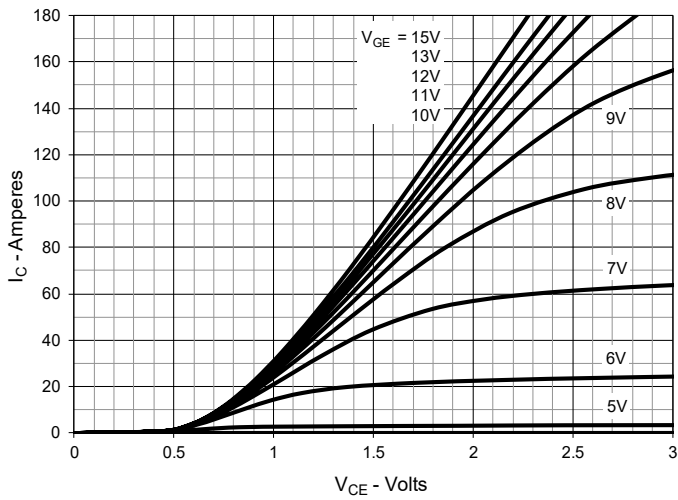
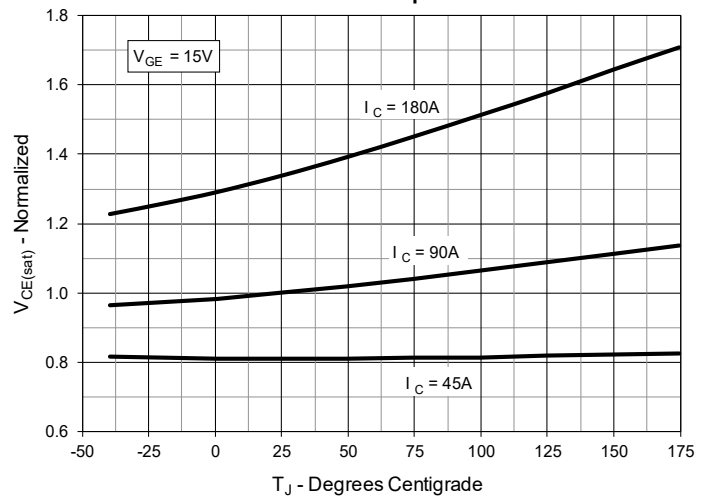
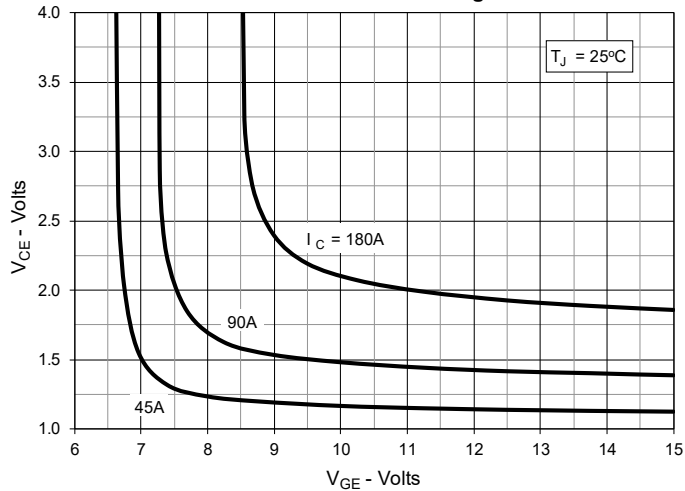
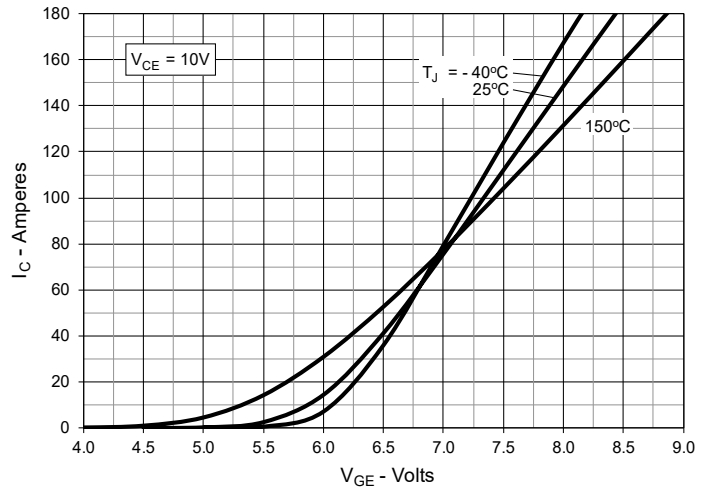
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 3. Output Characteristics @ $T_J = 150^\circ\text{C}$

Fig. 4. Dependence of $V_{CE(sat)}$ on Junction Temperature

Fig. 5. Collector-to-Emitter Voltage vs. Gate-to-Emitter Voltage

Fig. 6. Input Admittance


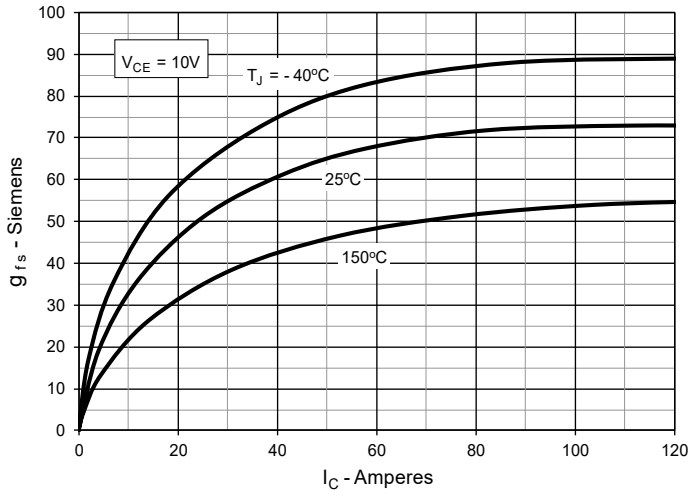
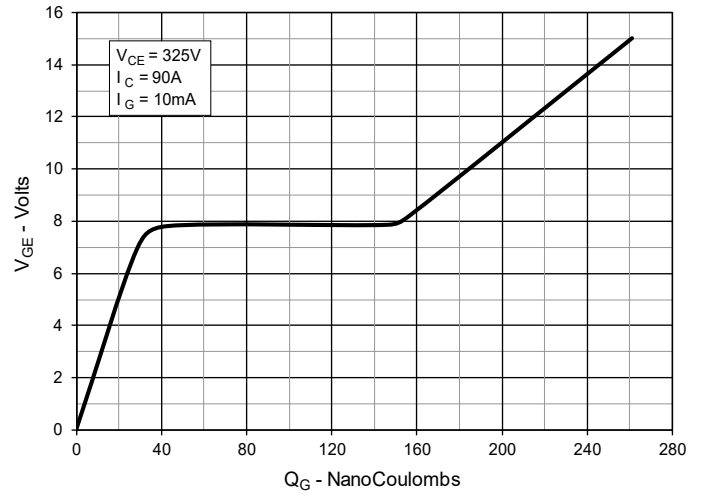
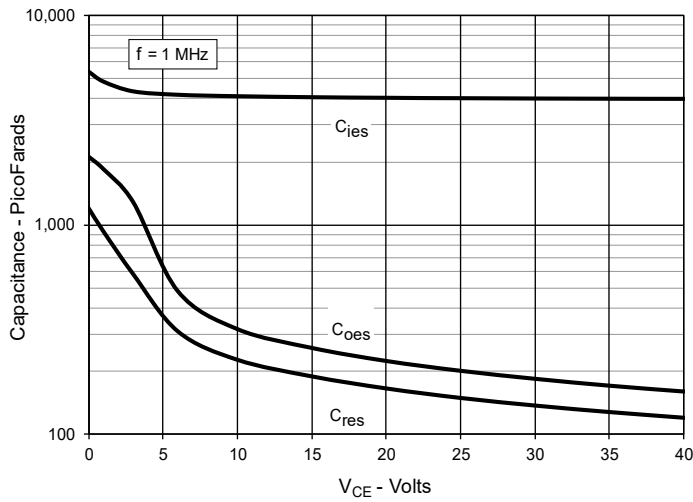
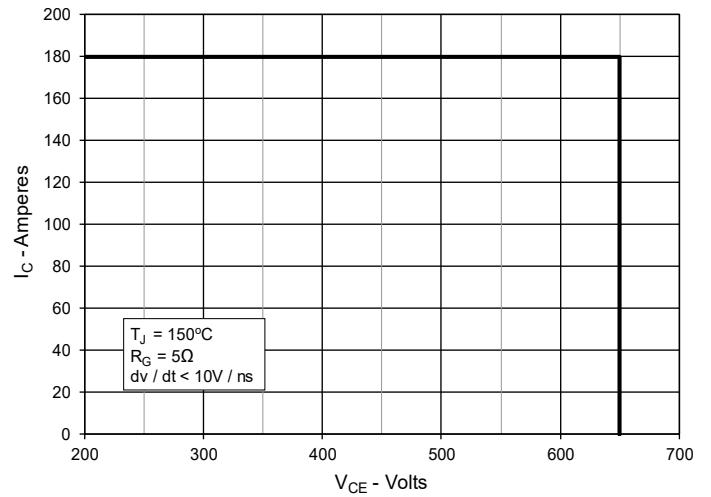
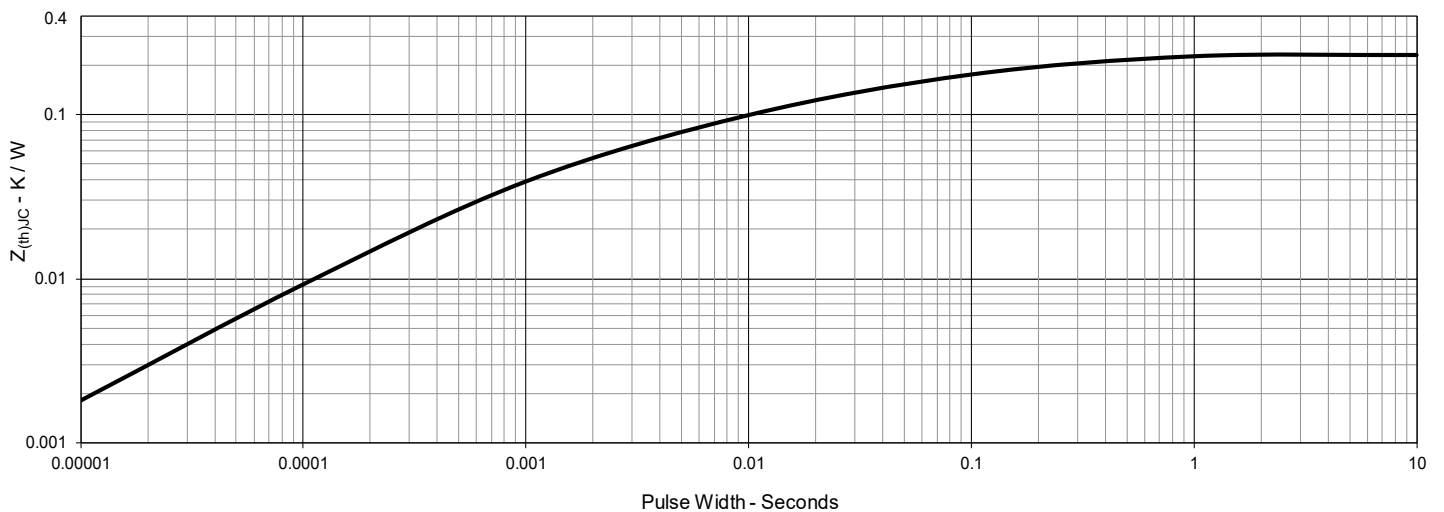
Fig. 7. Transconductance

Fig. 8. Gate Charge

Fig. 9. Capacitance

Fig. 10. Reverse-Bias Safe Operating Area

Fig. 11. Maximum Transient Thermal Impedance


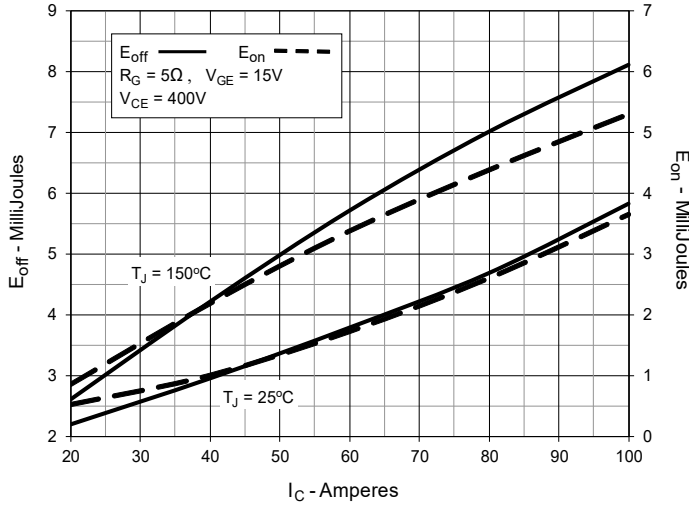
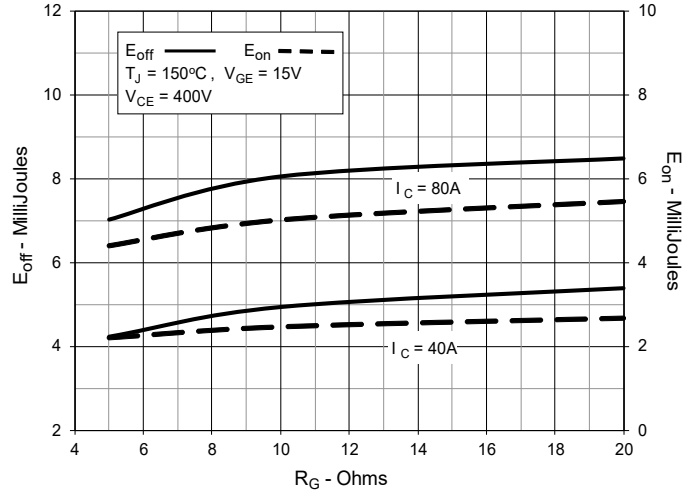
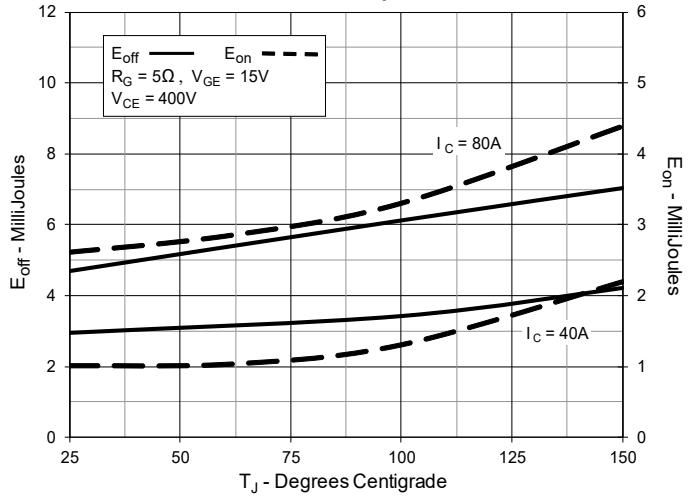
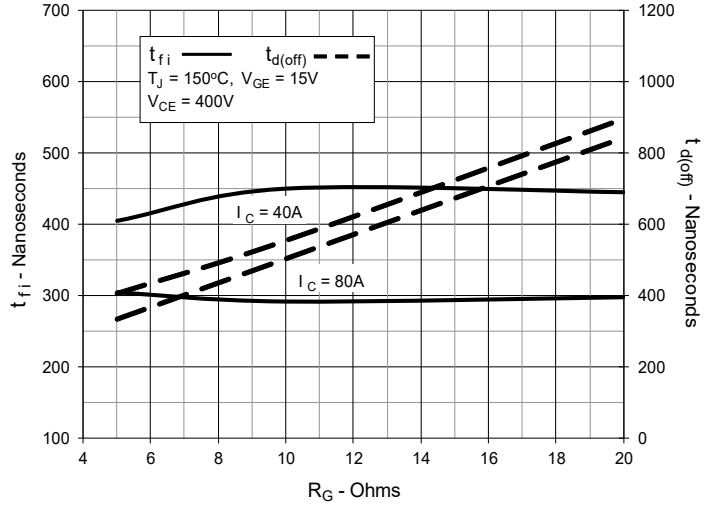
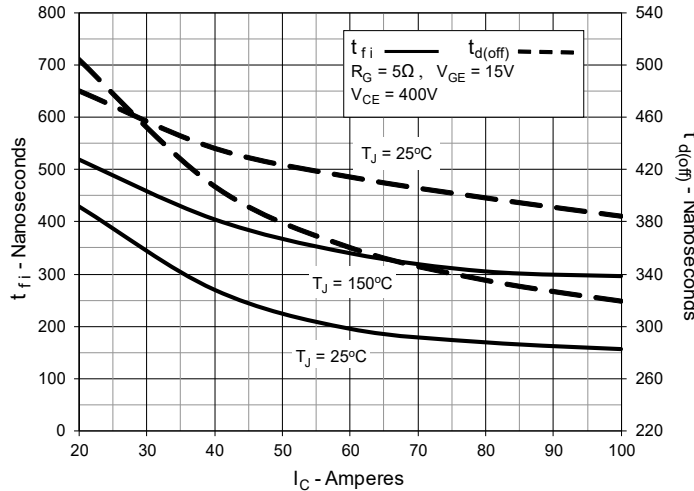
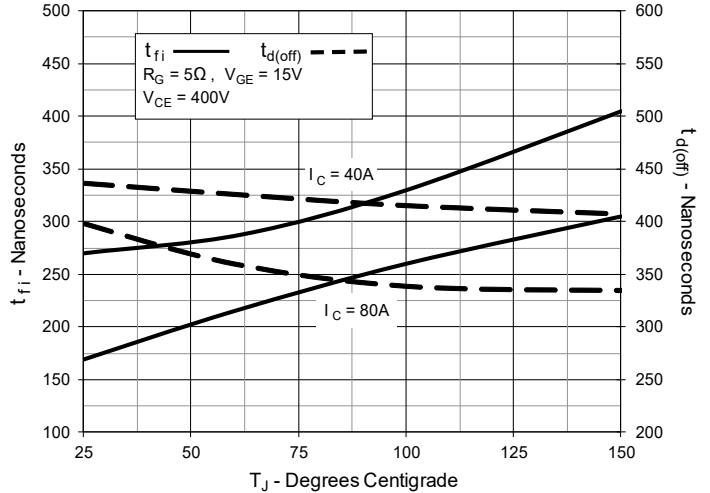
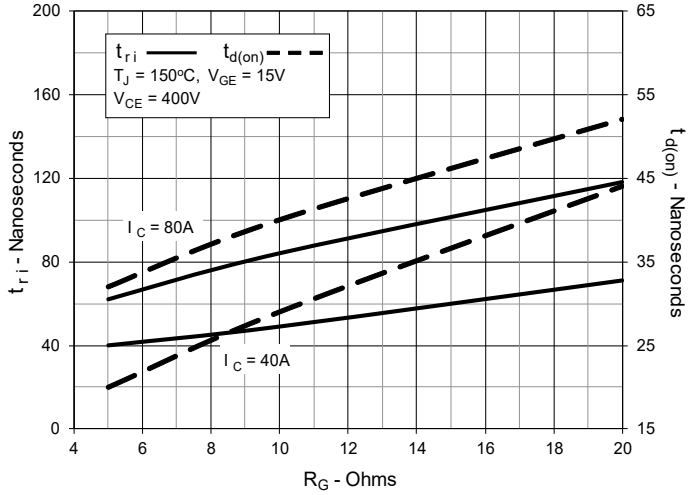
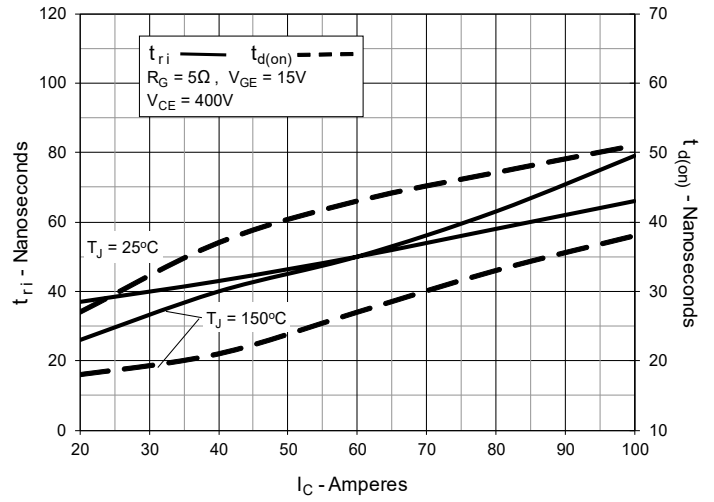
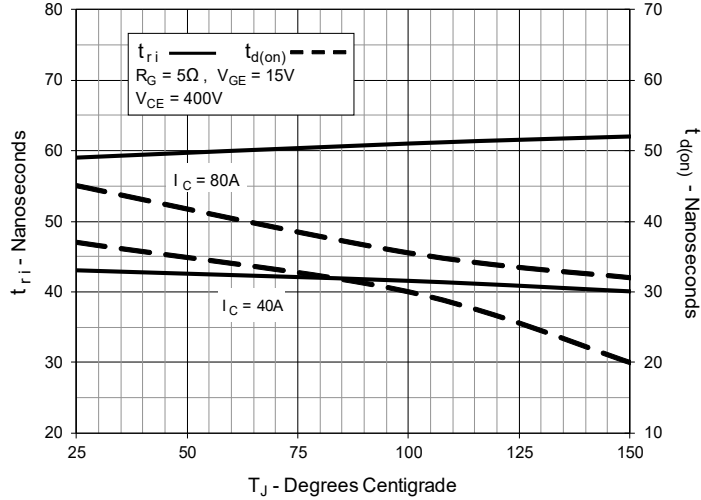
Fig. 12. Inductive Switching Energy Loss vs. Collector Current

Fig. 13. Inductive Switching Energy Loss vs. Gate Resistance

Fig. 14. Inductive Switching Energy Loss vs. Junction Temperature

Fig. 15. Inductive Turn-off Switching Times vs. Gate Resistance

Fig. 16. Inductive Turn-off Switching Times vs. Collector Current

Fig. 17. Inductive Turn-off Switching Times vs. Junction Temperature


Fig. 18. Inductive Turn-on Switching Times vs. Gate Resistance

Fig. 19. Inductive Turn-on Switching Times vs. Collector Current

Fig. 20. Inductive Turn-on Switching Times vs. Junction Temperature

Fig. 21. Maximum Peak Load Current vs. Frequency
