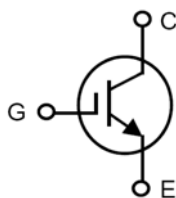


# XPT™ 650V IGBT GenX4™

## MMIX1X340N65B4

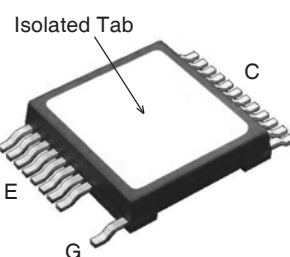
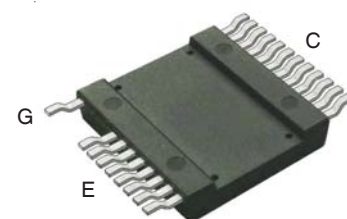
(Electrically Isolated Tab)

Extreme Light Punch Through  
IGBT for 10-30kHz Switching



$$\begin{aligned} V_{CES} &= 650V \\ I_{C90} &= 295A \\ V_{CE(sat)} &\leq 1.7V \\ t_{fi(typ)} &= 80ns \end{aligned}$$

Symbol	Test Conditions	Maximum Ratings	
$V_{CES}$	$T_J = 25^\circ\text{C}$ to $175^\circ\text{C}$	650	V
$V_{CGR}$	$T_J = 25^\circ\text{C}$ to $175^\circ\text{C}$ , $R_{GE} = 1M\Omega$	650	V
$V_{GES}$	Continuous	$\pm 20$	V
$V_{GEM}$	Transient	$\pm 30$	V
$I_{C25}$	$T_C = 25^\circ\text{C}$	450	A
$I_{C90}$	$T_C = 90^\circ\text{C}$	295	A
$I_{CM}$	$T_C = 25^\circ\text{C}$ , 1ms	1200	A
<b>SSOA</b> <b>(RBSOA)</b>	$V_{GE} = 15V$ , $T_{VJ} = 150^\circ\text{C}$ , $R_G = 1\Omega$ Clamped Inductive Load	$I_{CM} = 400$ @ $V_{CE} \leq V_{CES}$	A
$t_{sc}$ <b>(SCSOA)</b>	$V_{GE} = 15V$ , $V_{CE} = 360V$ , $T_J = 150^\circ\text{C}$ $R_G = 10\Omega$ , Non Repetitive	10	$\mu\text{s}$
$P_C$	$T_C = 25^\circ\text{C}$	1200	W
$T_J$		-55 ... +175	$^\circ\text{C}$
$T_{JM}$		175	$^\circ\text{C}$
$T_{stg}$		-55 ... +175	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering	300	$^\circ\text{C}$
$T_{SOLD}$	1.6 mm (0.062in.) from Case for 10s	260	$^\circ\text{C}$
$V_{ISOL}$	50/60Hz, 1 minute	2500	V~
$F_C$	Mounting Force	50..200/11..45	N/lb
<b>Weight</b>		8	g



G = Gate                      E = Emitter  
C = Collector

### Features

- Very High Current Capability
- Square RBSOA
- Short Circuit Capability
- Silicon Chip on Direct-Copper Bond (DCB) Substrate
- Isolated Mounting Surface
- 2500V~ Electrical Isolation
- Optimized for Low Conduction and Switching Losses

### Advantages

- High Power Density
- Low Gate Drive Requirement

### Applications

- Power Inverters
- UPS
- Motor Drives
- SMPS
- PFC Circuits
- Battery Chargers
- Welding Machines
- Lamp Ballasts

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{CES}$	$I_C = 250\mu\text{A}$ , $V_{GE} = 0V$	650		V
$V_{GE(th)}$	$I_C = 4\text{mA}$ , $V_{CE} = V_{GE}$	4.0		6.5 V
$I_{CES}$	$V_{CE} = V_{CES}$ , $V_{GE} = 0V$ $T_J = 150^\circ\text{C}$			25 $\mu\text{A}$ 2 mA
$I_{GES}$	$V_{CE} = 0V$ , $V_{GE} = \pm 20V$			$\pm 200$ nA
$V_{CE(sat)}$	$I_C = 160A$ , $V_{GE} = 15V$ , Note 1 $T_J = 150^\circ\text{C}$		1.4 1.4	1.7 V V

Symbol Test Conditions ( $T_J = 25^\circ\text{C}$ Unless Otherwise Specified)		Characteristic Values		
		Min.	Typ.	Max.
$g_{fs}$	$I_C = 60\text{A}, V_{CE} = 10\text{V}, \text{Note 1}$	50	85	S
$C_{ies}$	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$		11.25	nF
$C_{oes}$			670	pF
$C_{res}$			390	pF
$Q_{g(on)}$	$I_C = 200\text{A}, V_{GE} = 15\text{V}, V_{CE} = 0.5 \cdot V_{CES}$		553	nC
$Q_{ge}$			110	nC
$Q_{gc}$			253	nC
$t_{d(on)}$	<b>Inductive load, <math>T_J = 25^\circ\text{C}</math></b> $I_C = 100\text{A}, V_{GE} = 15\text{V}$ $V_{CE} = 400\text{V}, R_G = 1\Omega$ Note 2		62	ns
$t_{ri}$			76	ns
$E_{on}$			4.40	mJ
$t_{d(off)}$			245	ns
$t_{fi}$			80	ns
$E_{off}$			2.20	3.50 mJ
$t_{d(on)}$	<b>Inductive load, <math>T_J = 150^\circ\text{C}</math></b> $I_C = 100\text{A}, V_{GE} = 15\text{V}$ $V_{CE} = 400\text{V}, R_G = 1\Omega$ Note 2		54	ns
$t_{ri}$			65	ns
$E_{on}$			5.55	mJ
$t_{d(off)}$			236	ns
$t_{fi}$			110	ns
$E_{off}$			2.54	mJ
$R_{thJC}$			0.125	$^\circ\text{C/W}$
$R_{thCS}$		0.15		$^\circ\text{C/W}$

**Notes:**

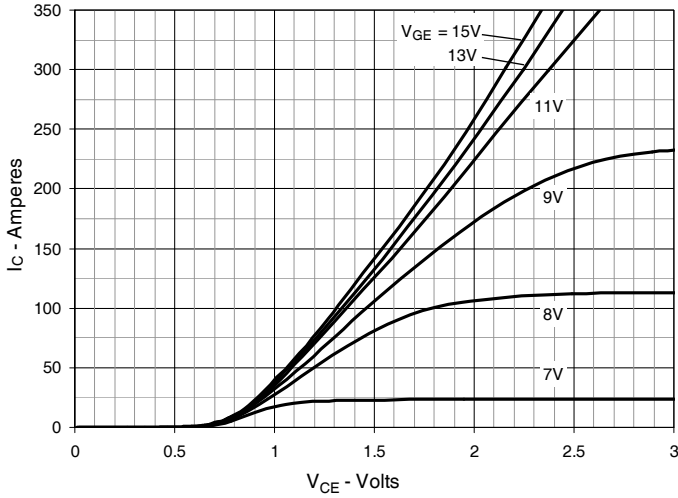
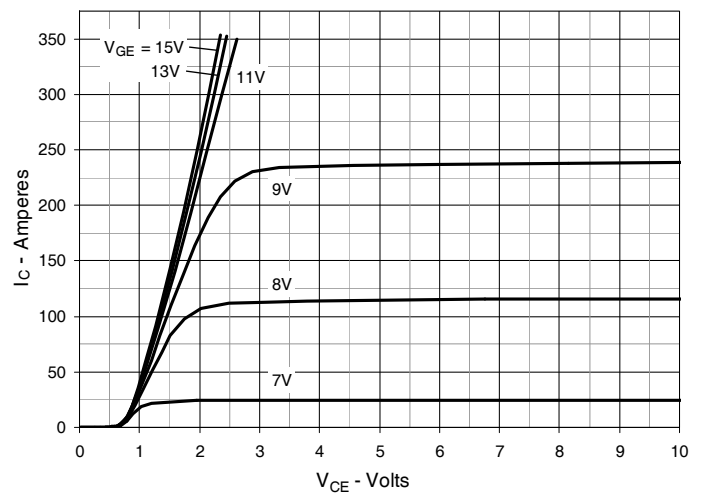
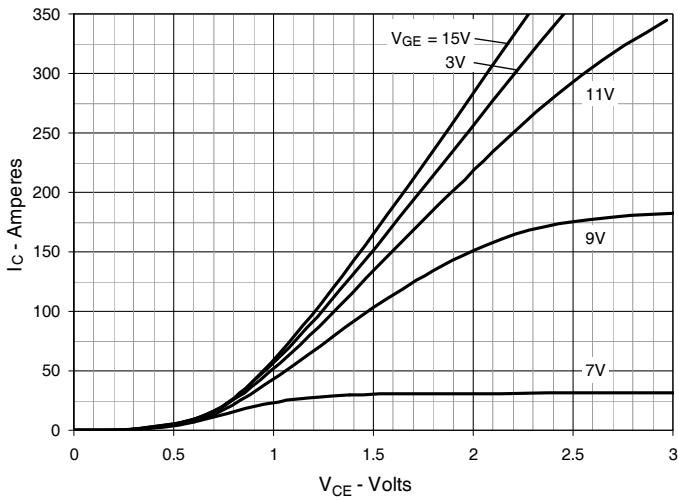
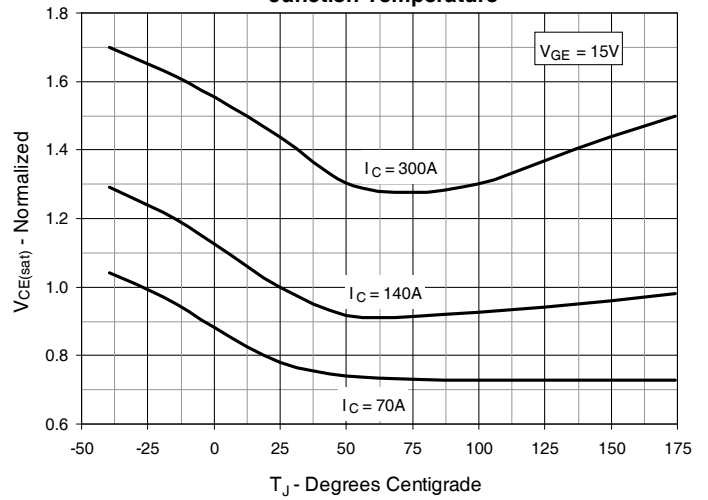
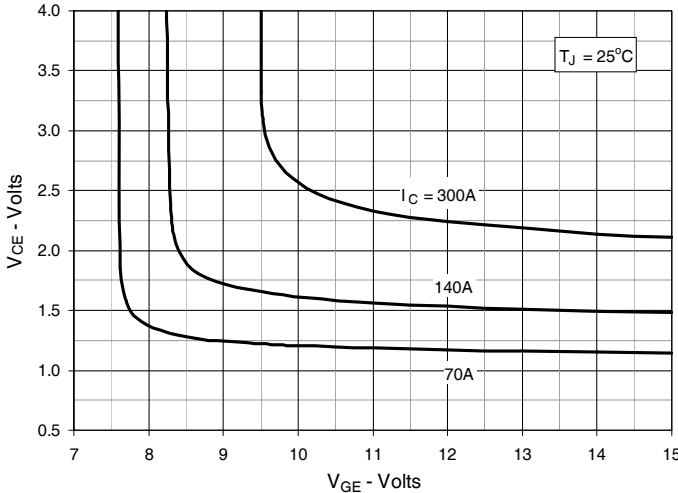
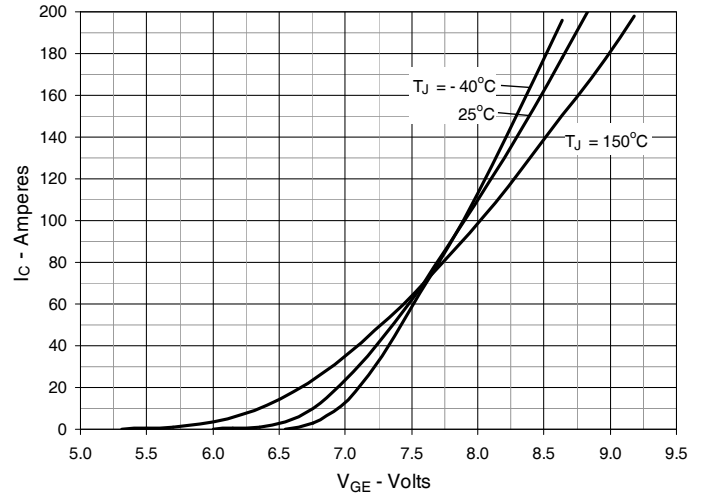
1. Pulse test,  $t \leq 300\mu\text{s}$ , duty cycle,  $d \leq 2\%$ .
2. Switching times & energy losses may increase for higher  $V_{CE}(\text{clamp})$ ,  $T_J$  or  $R_G$ .

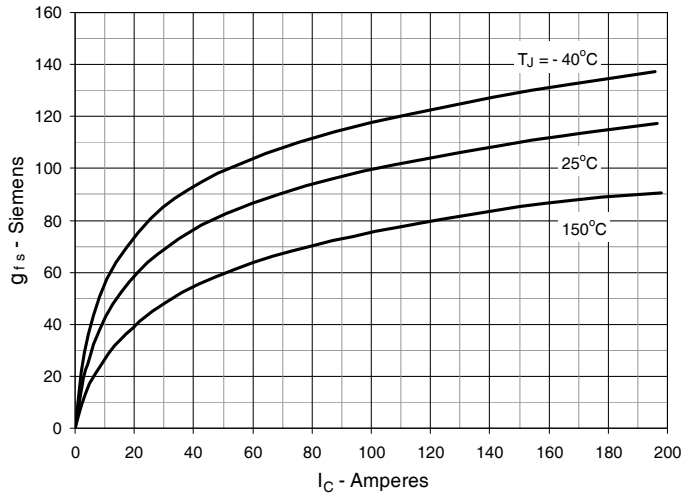
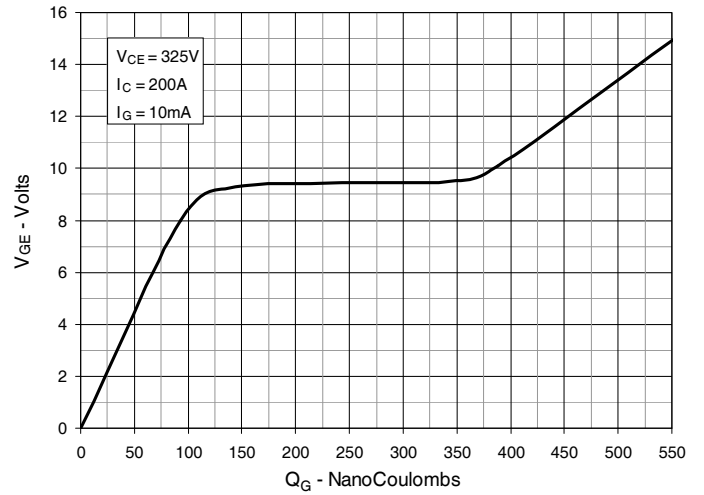
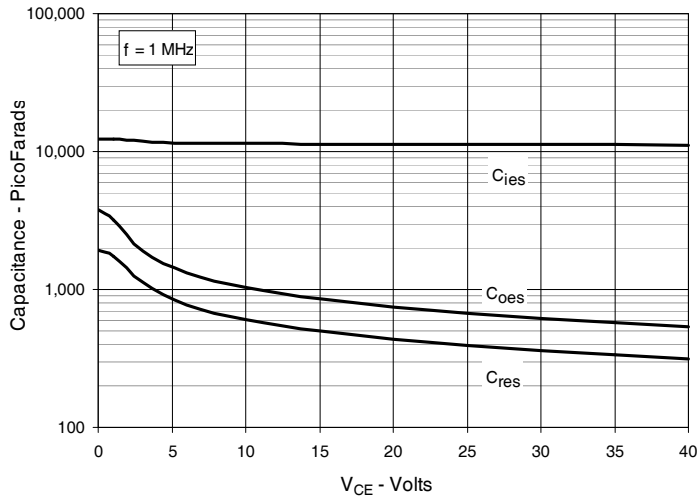
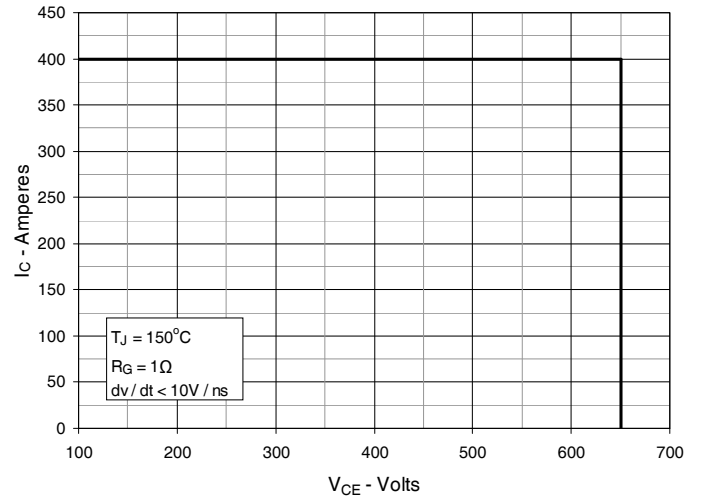
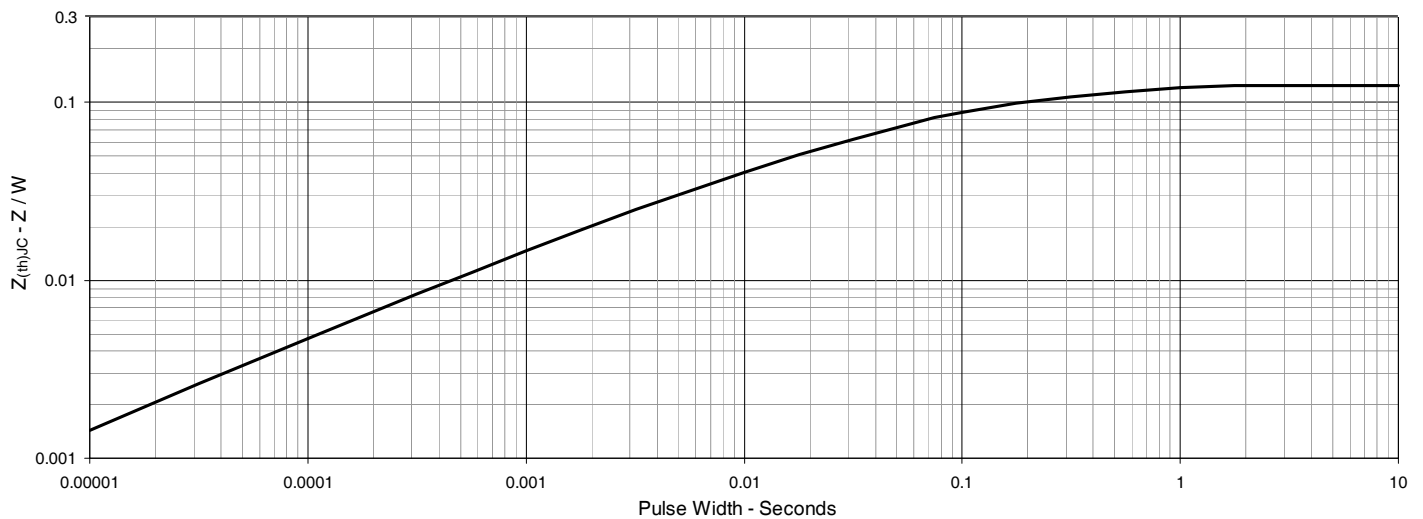
**PRELIMINARY TECHNICAL INFORMATION**

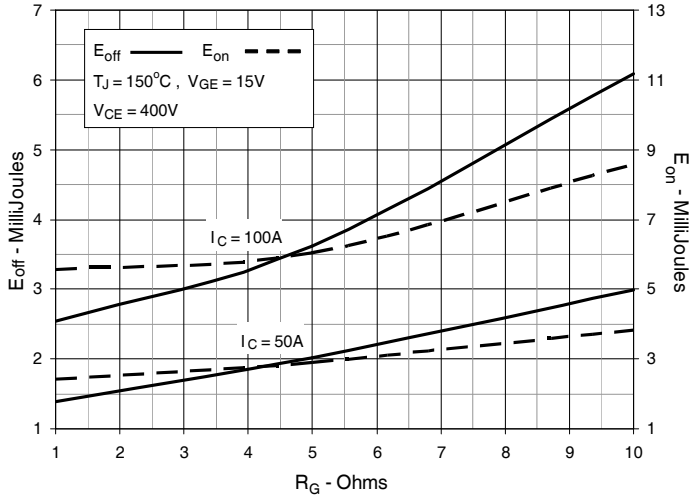
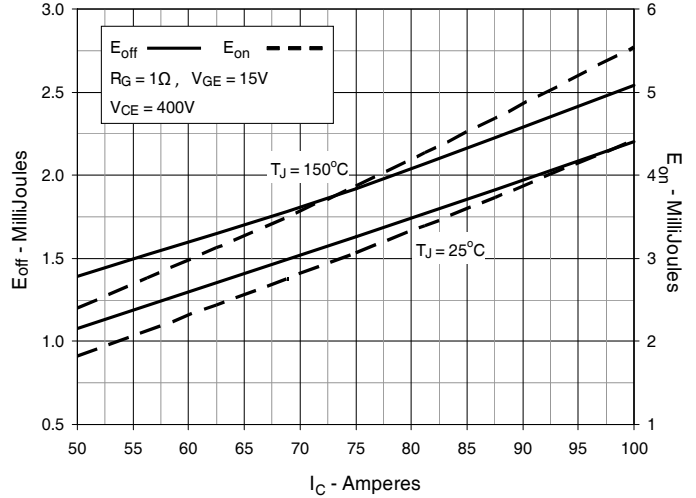
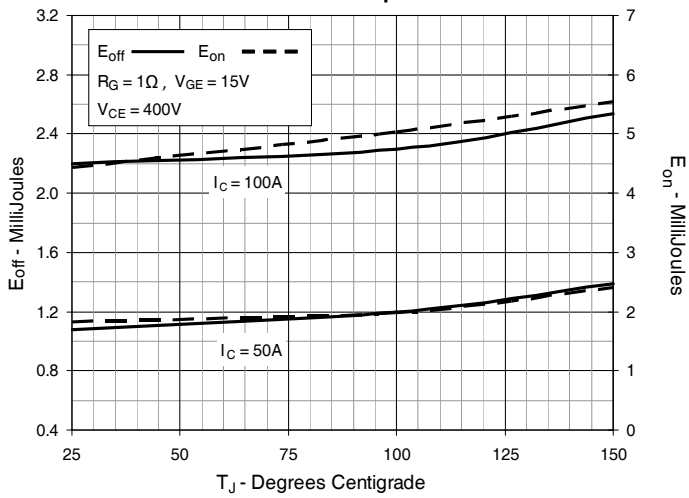
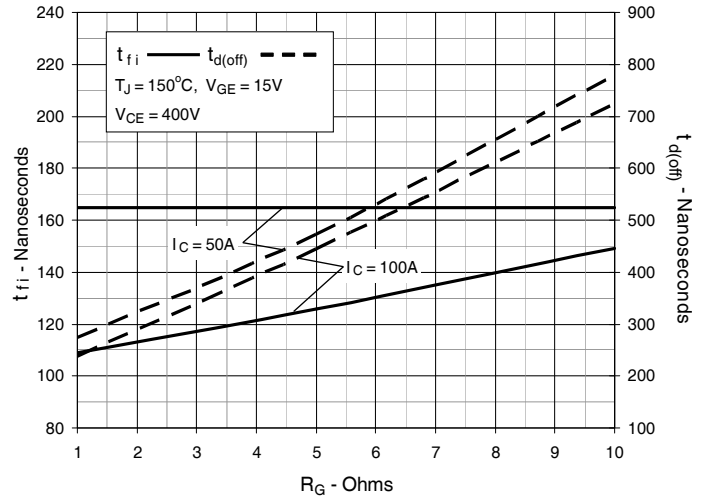
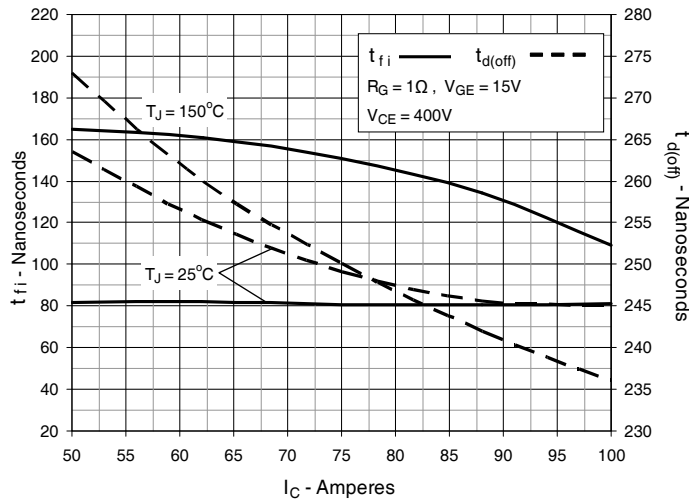
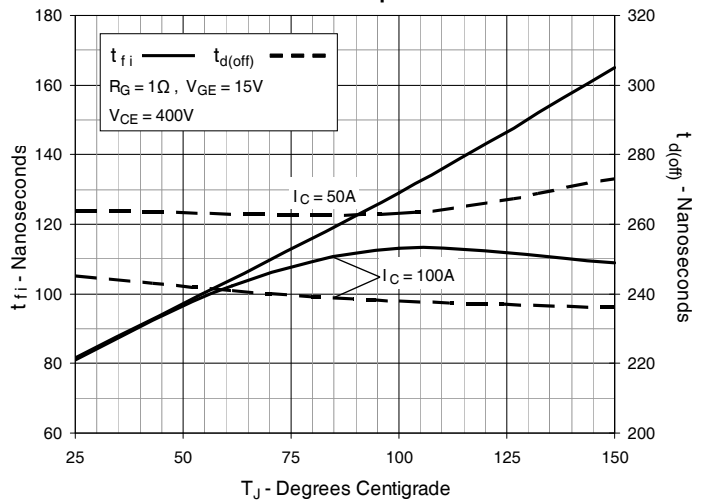
The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

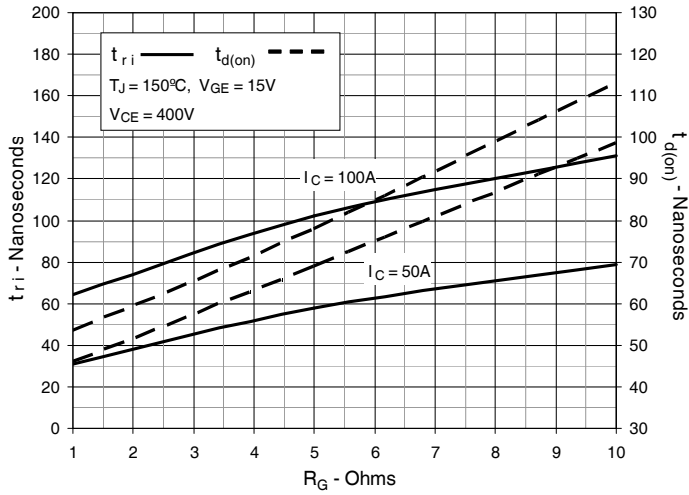
IXYS MOSFETs and IGBTs are covered	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2	7,157,338B2
by one or more of the following U.S. patents:	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2	7,071,537	

**Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$** 

**Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$** 

**Fig. 3. Output Characteristics @  $T_J = 150^\circ\text{C}$** 

**Fig. 4. Dependence of  $V_{CE(sat)}$  on Junction Temperature**

**Fig. 5. Collector-to-Emitter Voltage vs. Gate-to-Emitter Voltage**

**Fig. 6. Input Admittance**


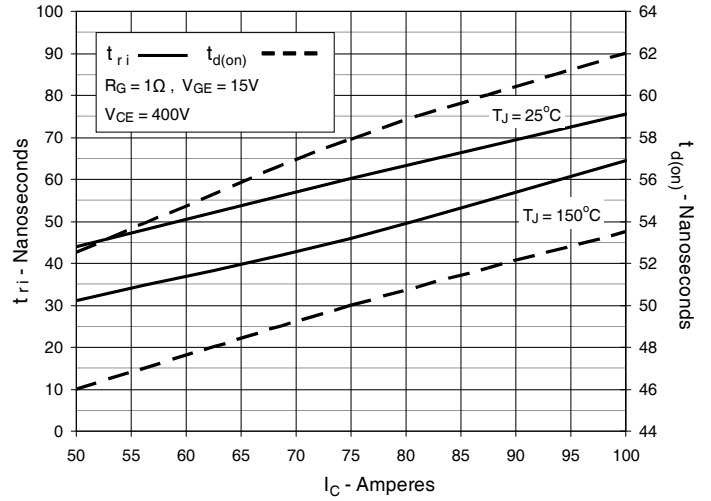
**Fig. 7. Transconductance**

**Fig. 8. Gate Charge**

**Fig. 9. Capacitance**

**Fig. 10. Reverse-Bias Safe Operating Area**

**Fig. 11. Maximum Transient Thermal Impedance**


**Fig. 12. Inductive Switching Energy Loss vs. Gate Resistance**

**Fig. 13. Inductive Switching Energy Loss vs. Collector Current**

**Fig. 14. Inductive Switching Energy Loss vs. Junction Temperature**

**Fig. 15. Inductive Turn-off Switching Times vs. Gate Resistance**

**Fig. 16. Inductive Turn-off Switching Times vs. Collector Current**

**Fig. 17. Inductive Turn-off Switching Times vs. Junction Temperature**


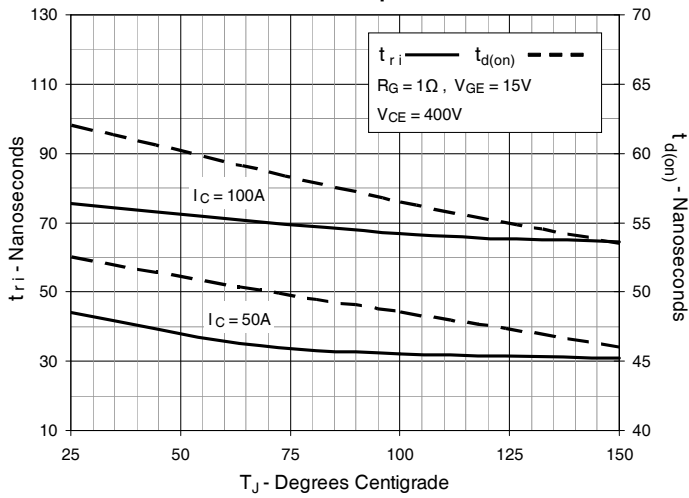
**Fig. 18. Inductive Turn-on Switching Times vs. Gate Resistance**



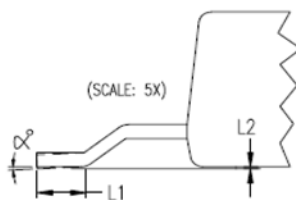
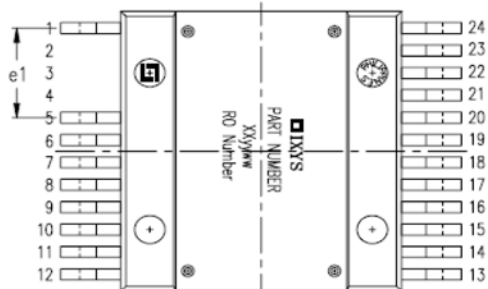
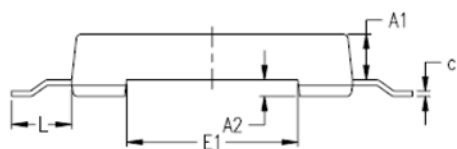
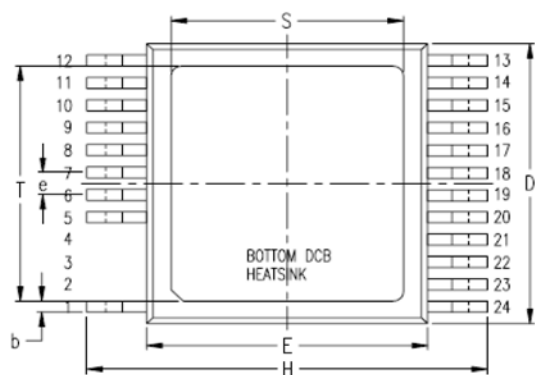
**Fig. 19. Inductive Turn-on Switching Times vs. Collector Current**



**Fig. 20. Inductive Turn-on Switching Times vs. Junction Temperature**



## Package Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.209	.224	5.30	5.70
A1	.154	.161	3.90	4.10
A2	.055	.063	1.40	1.60
b	.035	.045	0.90	1.15
c	.018	.026	0.45	0.65
D	.976	.994	24.80	25.25
E	.898	.915	22.80	23.25
E1	.543	.559	13.80	14.20
e	.079 BSC		2.00 BSC	
e1	.315 BSC		8.00 BSC	
H	1.272	1.311	32.30	33.30
L	.181	.209	4.60	5.30
L1	.051	.067	1.30	1.70
L2	.000	.006	0.00	0.15
S	.736	.760	18.70	19.30
T	.815	.839	20.70	21.30
α	0	4°	0	4°

**PIN:** 1 = Gate  
 5-12 = Emitter  
 13-24 = Collector



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