

Parameter	Rating	Units
Blocking Voltage	400	V _P
Load Current	100	mA _{rms} / mA _{DC}
On-Resistance (max)	35	Ω
LED Forward Current (to Activate)	2	mA

Features

- 1500V_{rms} Input/Output Isolation
- No EMI/RFI Generation
- Immune to Radiated EM Fields
- Tape & Reel Version Available
- Small 8-Pin SOIC Package
- Flammability Rating UL 94 V-0

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Security
 - Passive Infrared Detectors (PIR)
 - Data Signaling
 - Sensor Circuitry
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Medical Equipment—Patient/Equipment Isolation
- Industrial Controls

Description

The CPC2125N is a miniature device with two independent normally-closed (1-Form-B) solid state relays in an 8-pin SOIC package that employs optically coupled MOSFET technology to provide 1500V_{rms} of input to output isolation.

Optically coupled outputs, using the patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

Constructed using IXYS Integrated Circuits' state of the art double-molded, vertical construction packaging, this device is one of the world's smallest relays. It offers substantial board space savings over the competitor's larger 8-pin SOIC relay.

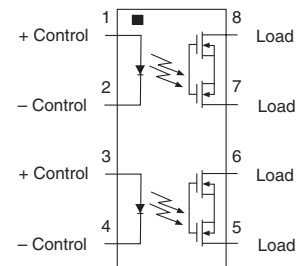
Approvals

- UL Recognized Component: File E76270
- TUV EN 62368-1: Certificate # B 082667 0008

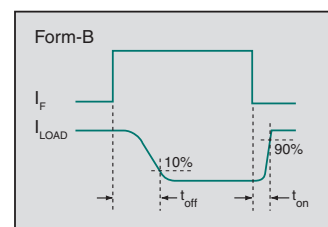
Ordering Information

Part #	Description
CPC2125N	8-Pin SOIC (50/tube)
CPC2125NTR	8-Pin SOIC (2000/reel)

Pin Configuration



Switching Characteristics of Normally-Closed (Form-B) Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	400	V _p
Reverse Input Voltage	5	V
LED Forward Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation	70	mW
Total Power Dissipation ¹	600	mW
Isolation Voltage, Input to Output (60 Seconds)	1500	V _{rms}
ESD Rating, Human Body Model	8	kV
Operational Temperature, Ambient	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 5mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ 25°C

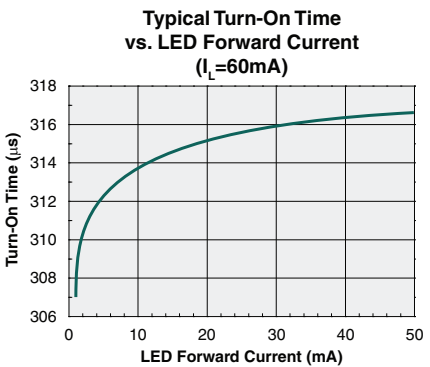
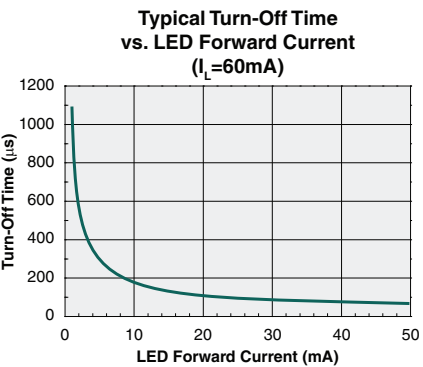
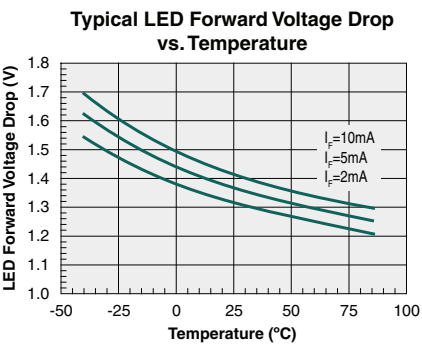
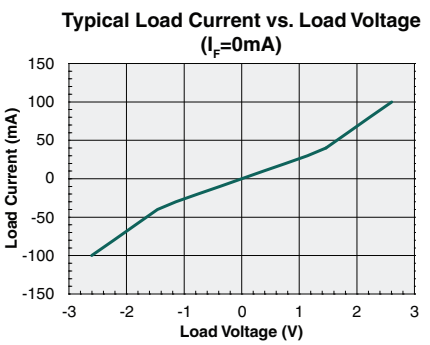
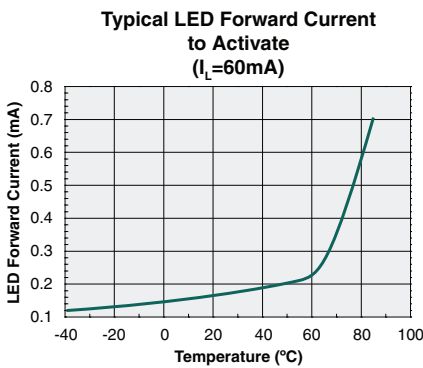
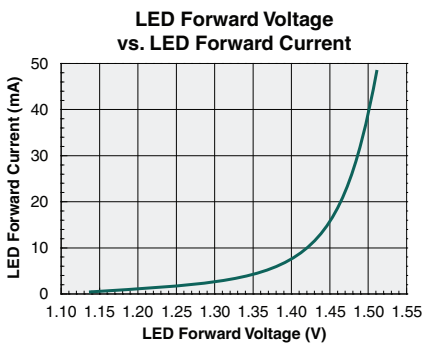
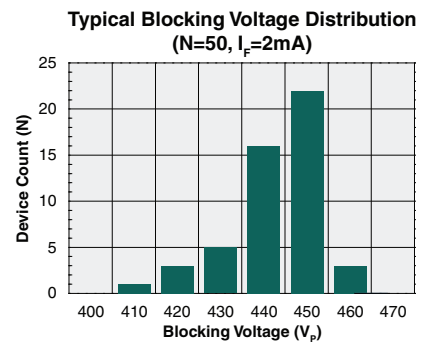
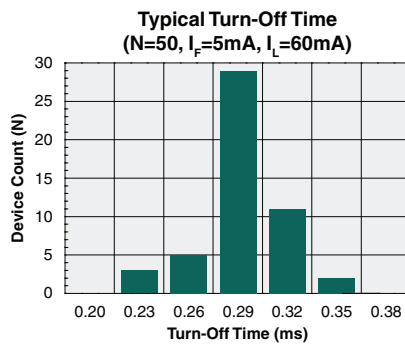
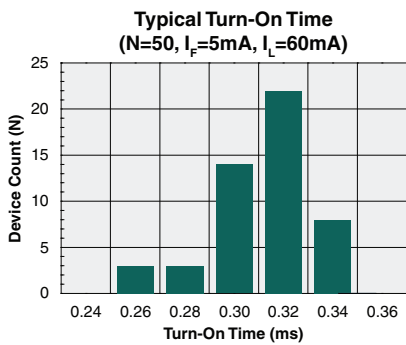
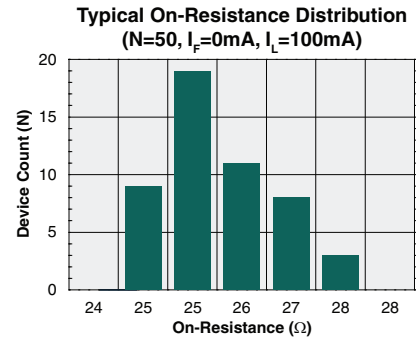
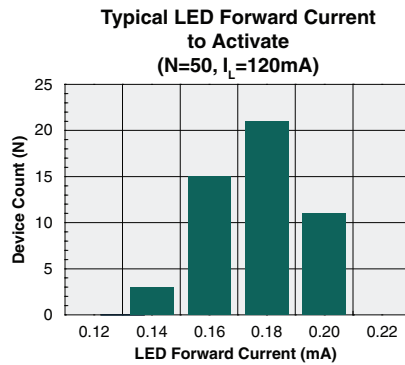
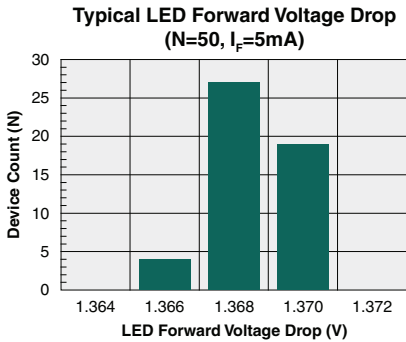
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage	I _L =1μA	V _{DRM}	400	-	-	V
Load Current						
Continuous ¹	I _F =0mA	I _L	-	-	100	mA _{rms} / mA _{DC}
Peak	t=10ms	I _{LPK}	-	-	±350	mA _p
On-Resistance ²	I _L =100mA	R _{ON}	-	26	35	Ω
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	0.31	2	ms
Turn-Off		t _{off}	-	0.30	2	
Off-State Leakage Current	V _L =400V, I _F =2mA	I _{LEAK}	-	-	5	μA
Output Capacitance	I _F =2mA, V _L =50V, f=1MHz	C _{OUT}	-	6	-	pF
Input Characteristics						
LED Forward Current						
To Activate ³	I _L =100mA	I _F	-	-	2	mA
To Deactivate			-	0.1	-	
Input Voltage Drop	I _F =5mA	V _F	0.9	1.36	1.5	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Capacitance, Input to Output	V _{IO} =0V, f=1MHz	C _{IO}	-	1	-	pF

¹ Load current derates linearly from 100mA @ 25°C to 60mA @ 85°C, and must be derated if both poles are operating simultaneously.

² Measurement taken within 1 second of on-time.

³ For applications requiring high temperature operation (greater than 60°C) a minimum LED forward current of 4mA is recommended.

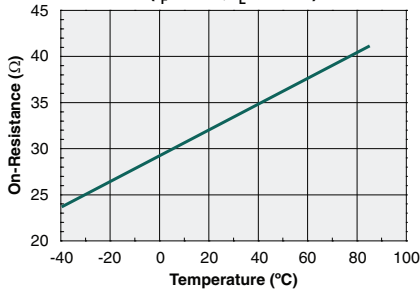
PERFORMANCE DATA*



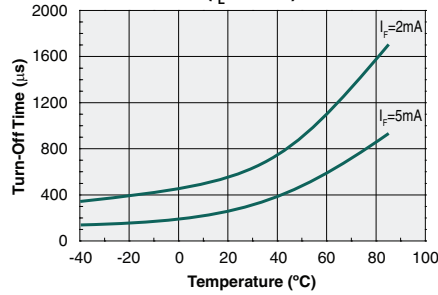
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

PERFORMANCE DATA*

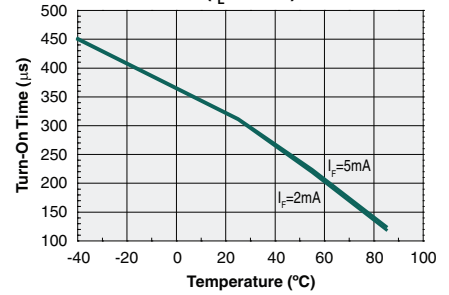
Typical On-Resistance vs. Temperature
($I_F=0\text{mA}$, $I_L=60\text{mA}$)



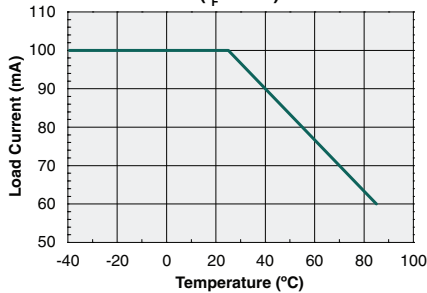
Typical Turn-Off Time vs. Temperature
($I_L=60\text{mA}$)



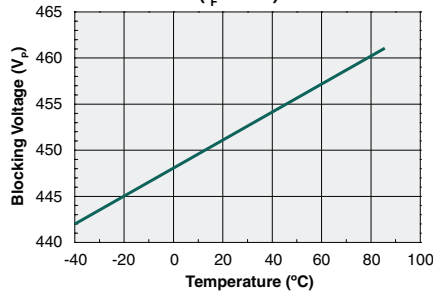
Typical Turn-On Time vs. Temperature
($I_L=60\text{mA}$)



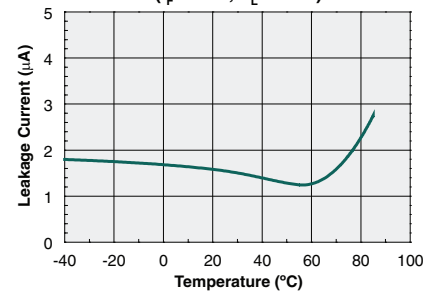
Maximum Load Current vs. Temperature
($I_F=0\text{mA}$)



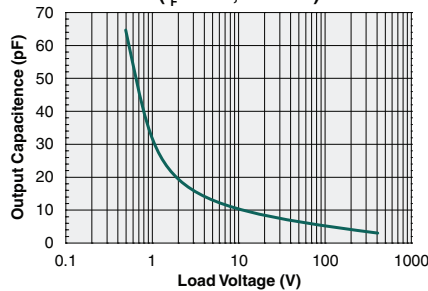
Typical Blocking Voltage vs. Temperature
($I_F=5\text{mA}$)



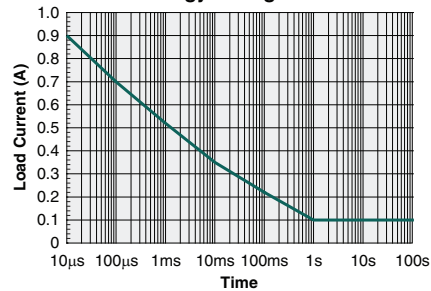
Leakage Current vs. Temperature Measured Across Pins 5&6, 7&8
($I_F=2\text{mA}$, $V_L=400\text{V}$)



Output Capacitance vs. Load Voltage
($I_F=2\text{mA}$, $f=1\text{MHz}$)



Energy Rating Curve



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
CPC2125N	MSL 3

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum dwell time the body temperature of these surface mount devices may be ($T_C - 5$)°C or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature (T_C)	Dwell Time (t_p)	Max Reflow Cycles
CPC2125N	260°C	30 seconds	3

Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.



